



GAIA TECH

RS1A THRU RS1M

GLASS PASSIVATED FAST RECOVERY RECTIFIER

Reverse Voltage: 50 to 1000 Volts

Forward Current:1.0Ampere

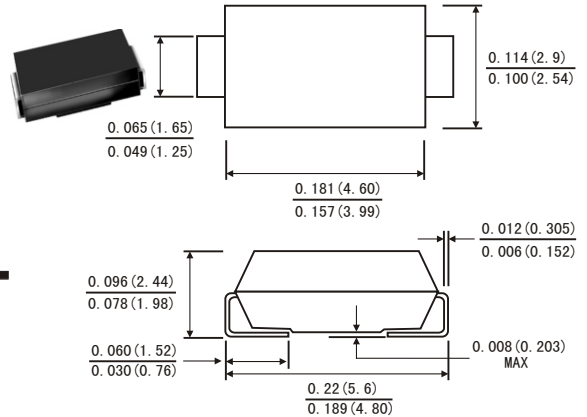
FEATURES

- Glass passivated junction
- For Surface Mount Applications, Easy to pick and place
- Plastic package has Underwriters Laboratory Flammability Classification 94V-0
- High temperature soldering guaranteed:260°C/10 seconds at terminals,
- Component in accordance to RoHS 2002/95/EC and WEEE 2002/96/EC

MECHANICAL DATA

- Case: JEDEC DO-214AC molded plastic over glass passivated chip
- Terminals: Solder plated
- Polarity: Color band denotes cathode end
- Weight: 0.002 ounce, 0.064 g

SMA(DO-214AC)



Dimensions in inches and (millimeters)

MAXIMUM RATINGS AND ELECTRICAL CHARACTERISTICS

(Rating at 25°C ambient temperature unless otherwise specified.Single phase ,half wave ,60Hz,resistive or inductive load. For capacitive load,derate current by 20%.)

		Symbols	RS1A	RS1B	RS1D	RS1G	RS1J	RS1K	RS1M	Units
Maximum Recurrent Peak Reverse Voltage		VRRM	50	100	200	400	600	800	1000	Volts
Maximum RMS Voltage		VRMS	35	70	140	280	420	560	700	Volts
Maximum DC Blocking Voltage		VDC	50	100	200	400	600	800	1000	Volts
Maximum Average Forward Rectified Current at TL=100°C		I(AV)	1.0							Amps
Peak Forward Surge Current 8.3ms single half sine-wave superimposed on rated load (JEDEC method)		IFSM	30.0							Amps
Maximum Instantaneous Forward Voltage at 1.0 A		VF	1.3							Volts
Maximum DC Reverse Current at rated DC blocking voltage	TA=25°C	IR	5.0							μA
	TA=125°C		150							
Maximum reverse recovery time(Note1)		trr	150				250	500		ns
Typical junction capacitance(Note2)		CJ	15.0							pF
Operating junction and storage temperature range		TJ TSTG	-55 to +175							°C



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RATINGS AND CHARACTERISTIC CURVES RS1A THRU RS1M

FIG.1-TYPICAL FORWARD CURRENT DERATING CURVE

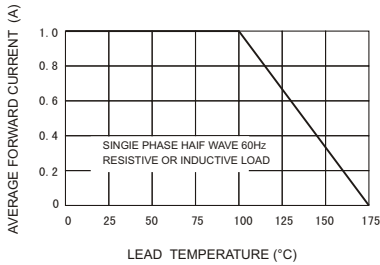


FIG.2-MAXIMUM NON-REPETITIVE FORWARD SURGE CURRENT

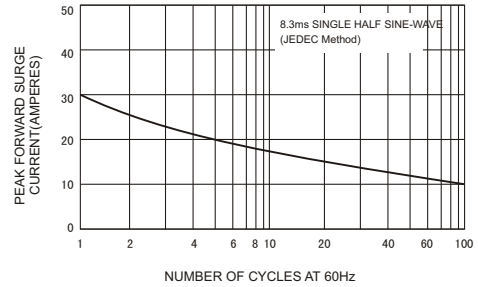


FIG.3-TYPICAL INSTANTANEOUS FORWARD CHARACTERISTICS

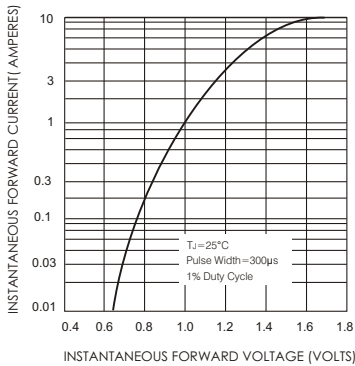


FIG.4-TYPICAL REVERSE CHARACTERISTICS

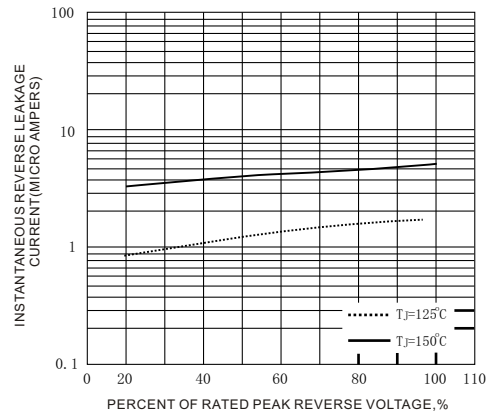


FIG.5-TYPICAL JUNCTION CAPACITANCE

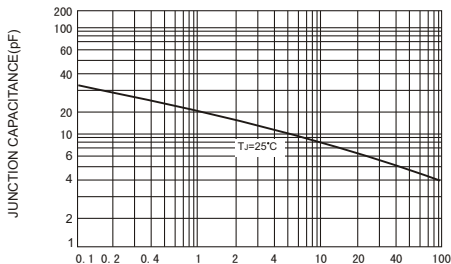


FIG.6-TEST CIRCUIT DIAGRAM AND REVERSE RECOVERY TIME CHARACTERISTIC

