



Film Capacitors

EMI Suppression Capacitors (MKP)

Series/Type: B3292*M3/N3

Date: October 2025

Applications

- X2 class for interference suppression
- "Across the line" application
- Severe ambient conditions
- For connections in series with the mains

Climatic

- Max. operating temperature: 110 °C
- Climatic category (IEC 60068-1): 40/110/56

Features

- Small dimensions
- Good self-healing properties
- AEC-Q200E compliant
- RoHS-compatible
- Halogen-free capacitors available on request

Construction

- Dielectric: metallised polypropylene (MKP)
- Plastic case (UL 94 V-0)
- Epoxy resin sealing (UL 94 V-0)

Terminals

- Parallel wire leads, lead-free tinned

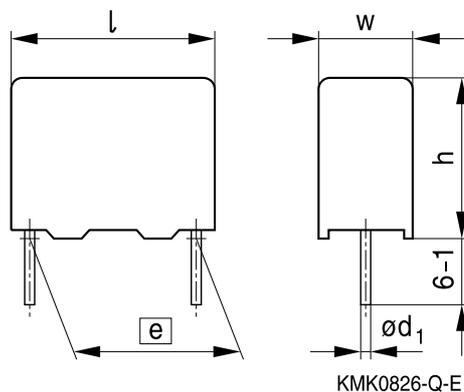
Marking

Manufacturer's logo, lot number, date code, rated capacitance (coded), capacitance tolerance (code letter), rated AC voltage, series number, sub-class (X2), dielectric code (MKP), climatic category, passive flammability category, approvals

Delivery mode

- Bulk (untaped)
- Taped (Ammo pack or reel)
For taping details, refer to document "Taping and packing".



Dimensional drawing


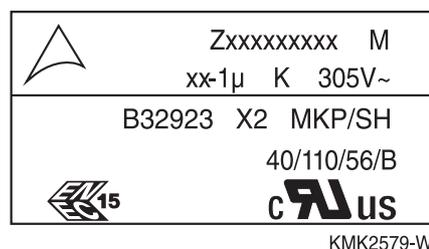
Dimensions in mm

Dimensions and types

Pin	Lead spacing $e \pm 0.4$	Lead diameter $d_1 \pm 0.05$	Type
2	15	0.8	B32922M/N3
2	22.5	0.8	B32923M/N3
2	27.5	0.8	B32924M/N3
2	37.5	1.0	B32926M/N3

Marking example

(position of marks may vary)


Approvals

Approval marks	Standards	Certificate
	EN 60384-14:2014 IEC 60384-14:2013	ENEC-05166 (approved by UL)
	UL 60384-14:2014 CSA E60384-14:2013	E97863 (approved by UL)

Overview of available types

Lead spacing	15 mm	22.5 mm	27.5 mm	37.5 mm
Type	B32922	B32923	B32924	B32926
C_R (μF)				
0.1				
0.12				
0.15				
0.18				
0.22				
0.27				
0.33				
0.39				
0.47				
0.56				
0.68				
0.82				
1.0				
1.5				
1.8				
2.2				
2.7				
3.3				
3.9				
4.7				
2.2				
3.3				
4.7				

Ordering codes and packing units

Lead spacing mm	C _R μF	Max. dimensions w × h × l mm	Ordering code (composition see below)	Ammo pack pcs./MOQ	Reel pcs./MOQ	Untaped pcs./MOQ	Pin
15	0.1	5.0 × 10.5 × 18.0	B32922M3104+***	4680	5200	4000	2
	0.12	5.0 × 10.5 × 18.0	B32922M3124+***	4680	5200	4000	2
	0.15	5.0 × 10.5 × 18.0	B32922N3154M***	4680	5200	4000	2
	0.15	6.0 × 11.0 × 18.0	B32922M3154+***	3840	4400	4000	2
	0.18	6.0 × 11.0 × 18.0	B32922M3184M***	3840	4400	4000	2
	0.22	6.0 × 12.0 × 18.0	B32922M3224+***	3840	4400	4000	2
	0.27	7.0 × 12.5 × 18.0	B32922M3274M***	3320	3600	4000	2
	0.33	8.0 × 14.0 × 18.0	B32922N3334+***	2920	3000	2000	2
	0.39	8.5 × 14.5 × 18.0	B32922M3394+***	2720	2800	2000	2
	0.47	8.5 × 14.5 × 18.0	B32922M3474M***	2720	2800	2000	2
	0.47	9.0 × 17.5 × 18.0	B32922N3474+***	2560	2800	2000	2
	0.56	9.0 × 17.5 × 18.0	B32922M3564K***	2560	2800	2000	2
	0.68	11.0 × 18.5 × 18.0	B32922M3684+***	2240	2200	1200	2
	0.82	11.0 × 18.5 × 18.0	B32922M3824+***	2240	2200	1200	2
	1.00	11.0 × 18.5 × 18.0	B32922M3105M***	2240	2200	1200	2

MOQ = Minimum Order Quantity, consisting of 4 packing units.
Further intermediate capacitance values on request.

Composition of ordering code

+ = Capacitance tolerance code:

M = ±20%

K = ±10%

*** = Packaging code

289 = Straight terminals, Ammo pack

189 = Straight terminals, Reel

003 = Straight terminals, untaped
(lead length 3.2 ±0.3 mm)

000 = Straight terminals, untaped
(lead length 6.0 –1.0 mm)

Ordering codes and packing units

Lead spacing mm	C _R μF	Max. dimensions w × h × l mm	Ordering code (composition see below)	Ammo pack pcs./MOQ	Reel pcs./MOQ	Untaped pcs./MOQ	Pin
22.5	0.22	6.0 × 15.0 × 26.5	B32923M3224+***	2720	2800	2880	2
	0.27	6.0 × 15.0 × 26.5	B32923M3274+***	2720	2800	2880	2
	0.33	6.0 × 15.0 × 26.5	B32923M3334+***	2720	2800	2880	2
	0.39	6.0 × 15.0 × 26.5	B32923M3394M***	2720	2800	2880	2
	0.47	7.5 × 14.0 × 26.5	B32923N3474+***	2200	2000	2280	2
	0.56	7.0 × 16.0 × 26.5	B32923M3564M***	2320	2400	2520	2
	0.68	8.5 × 16.5 × 26.5	B32923N3684+***	1920	2000	2040	2
	0.82	8.5 × 16.5 × 26.5	B32923M3824M***	1920	2000	2040	2
	1.0	10.5 × 16.5 × 26.5	B32923M3105+***	1560	1600	2160	2
	1.5	10.5 × 20.5 × 26.5	B32923M3155M***	1560	1600	2160	2
	1.5	12.0 × 22.0 × 26.5	B32923N3155+***	1320	1200	1800	2
	1.8	12.0 × 22.0 × 26.5	B32923M3185M***	1320	1200	1800	2
	2.2	12.0 × 29.0 × 26.5	B32923M3225+***	—	—	1200	2
	2.2	13.0 × 22.0 × 26.5	B32923N3225M***	—	—	880	2
	3.3	14.5 × 29.5 × 26.5	B32923M3335+***	—	—	1040	2

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003 = Straight terminals, untaped
(lead length 3.2 ±0.3 mm)

000 = Straight terminals, untaped
(lead length 6.0 –1.0 mm)

Ordering codes and packing units

Lead spacing mm	C _R μF	Max. dimensions w × h × l mm	Ordering code (composition see below)	Ammo pack pcs./MOQ	Reel pcs./MOQ	Untaped pcs./MOQ	Pin
27.5	1.5	11.0 × 19.0 × 31.5	B32924M3155+***	–	1400	1280	2
	1.8	11.0 × 19.0 × 31.5	B32924M3185M***	–	1400	1280	2
	2.2	12.5 × 21.5 × 31.5	B32924M3225+***	–	1200	1120	2
	2.7	14.0 × 24.5 × 31.5	B32924M3275+***	–	1000	1040	2
	3.3	15.0 × 24.5 × 31.5	B32924M3335+***	–	1000	960	2
	3.9	18.0 × 27.5 × 31.5	B32924M3395+***	–	–	800	2
	4.7	18.0 × 27.5 × 31.5	B32924M3475+***	–	–	800	2
37.5	2.2	12.0 × 22.0 × 42.0	B32926M3225+***	–	–	1620	2
	3.3	12.0 × 22.0 × 42.0	B32926M3335+***	–	–	1620	2
	4.7	14.0 × 25.0 × 42.0	B32926M3475M***	–	–	1380	2

MOQ = Minimum Order Quantity, consisting of 4 packing units.

Further intermediate capacitance values on request.

Composition of ordering code

+ = Capacitance tolerance code:

M = ±20%

K = ±10%

*** = Packaging code

289 = Straight terminals, Ammo pack

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(lead length 3.2 ±0.3 mm)

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(lead length 6.0 –1.0 mm)

Technical data and specifications

Reference standard: UL / IEC 60384-14:2013/AMD1:2016.

 All data given at $T = 20\text{ °C}$ unless otherwise specified

Rated AC voltage V_{AC} (IEC 60384-14)	305 V AC (50/60 Hz)		
Maximum continuous DC voltage V_{DC} (at $T_{op} \leq 85\text{ °C}$)	630 V DC		
DC operating derating between 85 °C and 110 °C	1.5% / °C of V_{DC} derating compared to V_{DC} at 85 °C		
Max. operating temperature T_{op} ($T_{op} = T_A + \text{self-heating}$)	+110 °C		
Lower category temperature T_{min}	−40 °C		
DC test voltage ¹⁾	Between terminals: 1312 V DC, 2 s		
Dissipation factor $\tan \delta$ (in 10^{-3}) at 20 °C (upper limit values)	at	$C < 2.2\ \mu\text{F}$	$C_R \geq 2.2\ \mu\text{F}$
	1 kHz	1.0	2.0
Insulation resistance R_{ins} (in $G\Omega$) or time constant $\tau = C_R \cdot R_{ins}$ (in s) at 100 V DC, 20 °C , rel. humidity $\leq 65\%$ and for 60 s (minimum as-delivered values)	$C_R \leq 0.33\ \mu\text{F}$	$C_R > 0.33\ \mu\text{F}$	
	15 $G\Omega$	5000 s	
Passive flammability category	B		
Capacitance tolerances (measured at 1 kHz)	$\pm 10\%$ (K), $\pm 20\%$ (M)		
Biased humidity test Lead spacing 15 mm	Temperature:	+85 °C $\pm 2\text{ °C}$	
	Relative humidity (RH):	85% $\pm 3\%$	
	Voltage value:	305 V AC, 50 Hz	
	Test duration:	500 hours	
Biased humidity test Lead spacing $\geq 22.5\text{ mm}$	Temperature:	+85 °C $\pm 2\text{ °C}$	
	Relative humidity (RH):	85% $\pm 3\%$	
	Voltage value:	305 V AC, 50 Hz	
	Test duration:	1000 hours	
Limit values after biased humidity test	Capacitance change $ \Delta C/C $:	$\leq 10\%$	
	Dissipation factor change $ \Delta \tan \delta $:	< 0.015 (at 1 kHz)	
	Insulation resistance R_{ins}	$\geq 200\ M\Omega$	

1) The repetition of this DC voltage test may damage the capacitor. Special care must be taken in case of use several capacitors in a parallel configuration.

Pulse handling capability

"dV/dt" represents the maximum permissible voltage change per unit of time for non-sinusoidal voltages, expressed in V/ μ s.

"k₀" represents the maximum permissible pulse characteristic of the waveform applied to the capacitor, expressed in V²/ μ s.

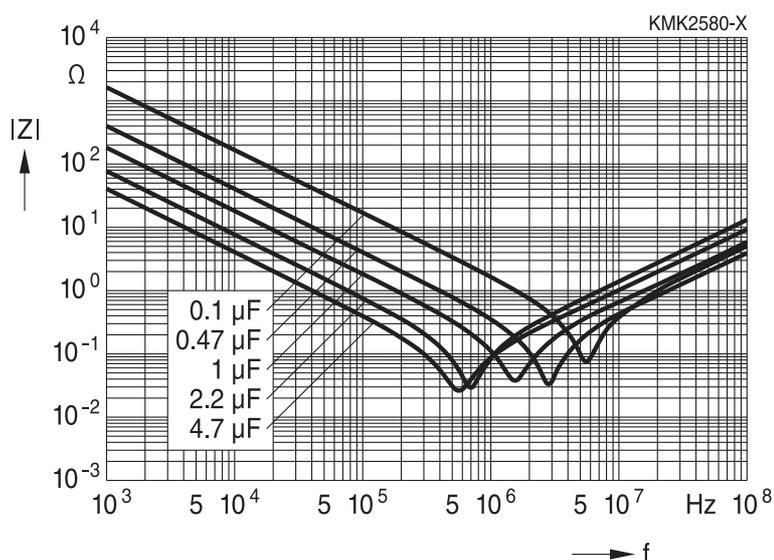
Note:

The values of dV/dt and k₀ provided below must not be exceeded in order to avoid damaging the capacitor. These parameters are given for isolated pulses in such a way that the heat generated by one pulse will be completely dissipated before applying the next pulse. For a train of pulses, please refer to the curves of permissible AC voltage-current versus frequency.

dV/dt and k₀ values

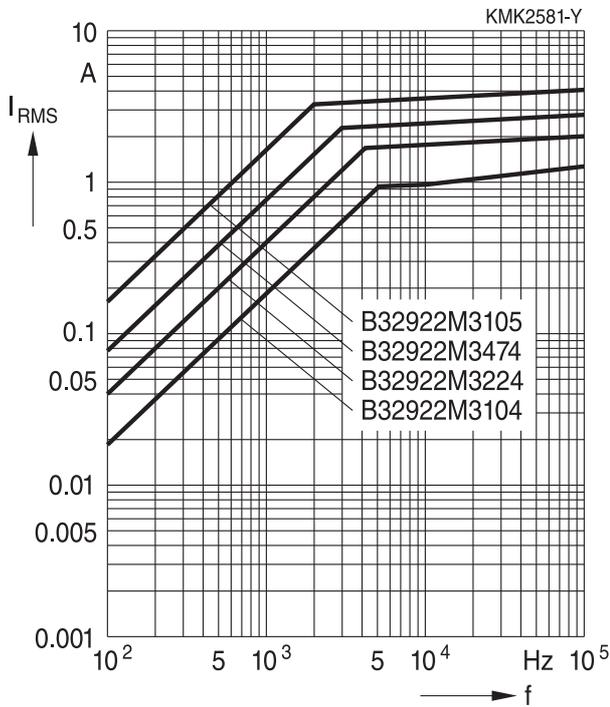
Lead spacing (mm)	15	22.5	27.5	37.5
dV/dt (V/ μ s)	300	140	100	70
k ₀ (V ² / μ s)	258000	120400	86000	60200

Impedance Z versus frequency f
(typical values)

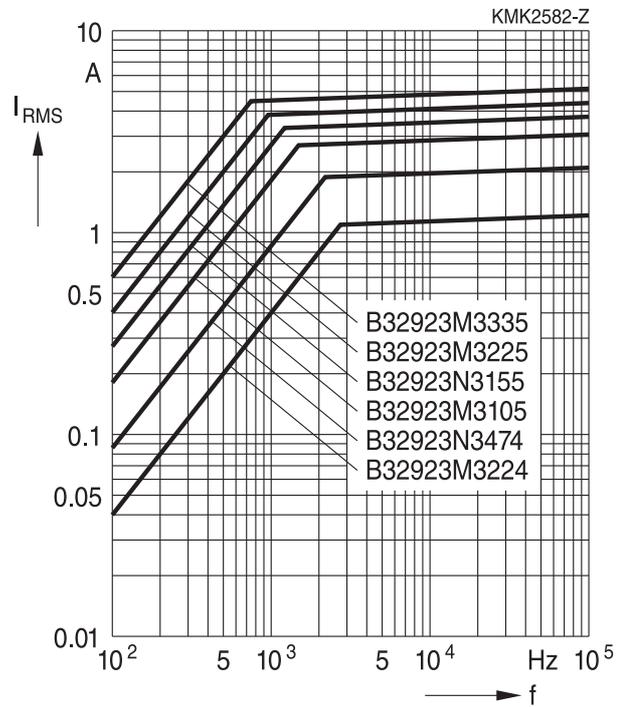


Permissible AC current I_{RMS} versus frequency f
 (for sinusoidal waveforms $T_A \leq 80^\circ C$ and $\Delta ESR < 100\%$ from receipt condition)

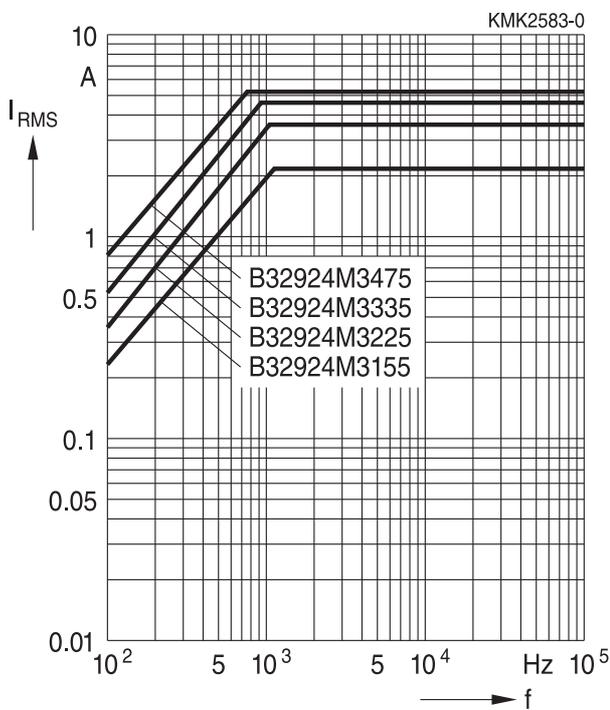
Lead spacing 15 mm



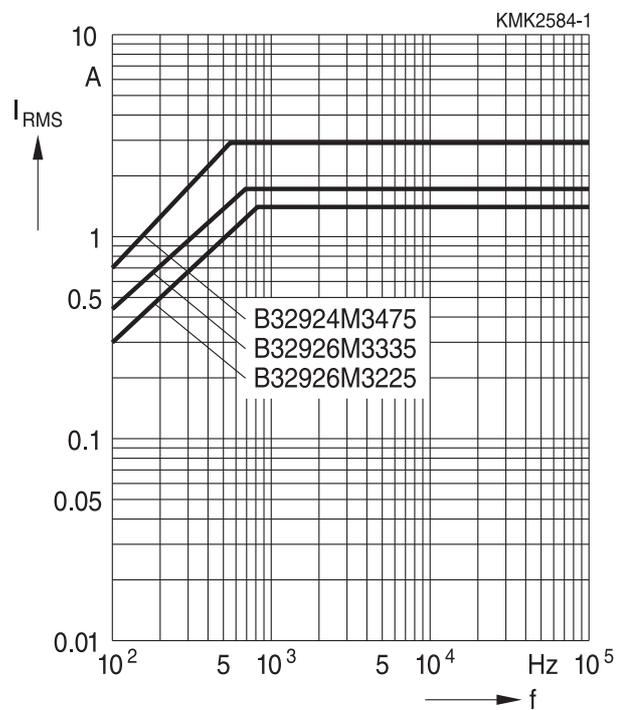
Lead spacing 22.5 mm



Lead spacing 27.5 mm



Lead spacing 37.5 mm



Testing and standards

Test	Reference	Conditions of test	Performance requirements
Electrical parameters	IEC 60384-14	Voltage Proof: Between terminals: $4.3 \times V_R$ (DC), 1 min Terminals and enclosure: $2 V_R + 1500$ V AC, 1 min Insulation resistance, R_{ins} Capacitance, C Dissipation factor, $\tan \delta$	Within specified limits
Robustness of terminations	IEC 60068-2-21	Tensile strength (test Ua1)	Capacitance and $\tan \delta$ within specified limits
		Wire diameter	
		$0.5 < d_1 \leq 0.8$ mm	10 N
		$0.8 < d_1 \leq 1.25$ mm	20 N
Resistance to soldering heat	IEC 60068-2-20, test Tb, method 1A	Solder bath temperature at 260 ± 5 °C, immersion for 10 seconds	$\Delta C/C_0 \leq 5\%$ $\tan \delta$ within specified limits
Rapid change of temperature	IEC 60384-14	T_A = lower category temperature T_B = upper category temperature Five cycles, duration $t = 30$ min	No visible damage $ \Delta C/C_0 \leq 5\%$ $\tan \delta$ within specified limits
Vibration	IEC 60384-14	Test F_C : vibration sinusoidal Displacement: 0.75 mm Acceleration: 98 m/s ² Frequency: 10 Hz ... 500 Hz Test duration: 3 orthogonal axes, 2 hours each axe	No visible damage
Bump	IEC 60384-14	Test Eb: Total 4000 bumps with 400 m/s ² mounted on PCB 6 ms duration	No visible damage $ \Delta C/C_0 \leq 5\%$ $\tan \delta$ within specified limits
Damp heat, steady state	IEC 60384-14	Test Ca 40 °C / 93% RH / 56 days	No visible damage $ \Delta C/C_0 \leq 5\%$ $ \Delta \tan \delta \leq 0.008$ for $C \leq 1$ μ F $ \Delta \tan \delta \leq 0.005$ for $C > 1$ μ F Voltage proof $R_{ins} \geq 50\%$ of initial limit
Impulse test Endurance	IEC 60384-14	3 impulses $T_B / 1.25 V_R / 1000$ hours, 1000 V_{RMS} for 0.1 s every hour	No visible damage $ \Delta C/C_0 \leq 10\%$ $ \Delta \tan \delta \leq 0.008$ for $C \leq 1$ μ F $ \Delta \tan \delta \leq 0.005$ for $C > 1$ μ F Voltage proof $R_{ins} \geq 50\%$ of initial limit

Test	Reference	Conditions of test	Performance requirements
Charge and discharge	IEC 60384-14	dv/dt = 100 V/μs Cycles = 10000	$ \Delta C/C_0 \leq 10\%$ $ \Delta \tan \delta \leq 0.008$ for $C \leq 1 \mu\text{F}$ $ \Delta \tan \delta \leq 0.005$ for $C > 1 \mu\text{F}$ $R_{\text{ins}} \geq 50\%$ of initial limit
Passive flammability	IEC 60384-14	Flame applied for a period of time depending on capacitor volume	B
Active flammability	IEC 60384-14	20 discharges at 2.5 kV + V_R	The cheesecloth shall not burn with a flame
Biased humidity test	—	Lead spacing: 15mm Temperature: +85°C ±2 °C Relative humidity (RH): 85% ±3% Voltage value: 305 V AC, 50 Hz Test duration: 500 hours	No visible damage $ \Delta C/C_0 \leq 10\%$ $ \Delta \tan \delta \leq 0.015$ (at 1 kHz) $R_{\text{ins}} \geq 200 \text{ M}\Omega$
		Lead spacing: ≥ 22.5mm Temperature: +85°C ±2 °C Relative humidity (RH): 85% ±3% Voltage value: 305 V AC, 50 Hz Test duration: 1000 hours	
High temperature exposure (storage)	AEC-Q200	110 °C for 1000 h	No visible damage $ \Delta C/C_0 \leq 10\%$ $ \Delta \tan \delta \leq 0.008$ $R_{\text{ins}} \geq 50\%$ of initial limit
Temperature cycling	AEC-Q200	$T_A = -40 \text{ }^\circ\text{C}$ $T_B =$ upper category temperature 1000 cycles, duration t = 30 min	No visible damage $ \Delta C/C_0 \leq 5\%$
Biased humidity	AEC-Q200	40 °C / 93% relative humidity / 1000 h / 305 V AC, 50 Hz	No visible damage $ \Delta C/C_0 \leq 10\%$ $ \Delta \tan \delta \leq 0.015$ $R_{\text{ins}} \geq 200 \text{ M}\Omega$
Operating life	AEC-Q200	1.25 × rated voltage / 110 °C / 1000 h (381V _{ac} / 110 °C / 1000 h)	No visible damage $ \Delta C/C_0 \leq 10\%$ $ \Delta \tan \delta \leq 0.008$ $R_{\text{ins}} \geq 50\%$ of initial limit
Physical dimension	AEC-Q200	As per user and supplier specification (length, width, height, lead length, lead space)	No visible damage Within specification
Resistance to soldering heat	AEC-Q200	260 ± 5 °C for 10 s	No visible damage $ \Delta C/C_0 \leq 5\%$ $ \Delta \tan \delta \leq 0.001$

Test	Reference	Conditions of test	Performance requirements
Solderability	AEC-Q200	J-STD-002, For both leaded and SMD Electrical test not required Magnification 50 X Conditions: leaded; method A at 235 °C, category 3	A minimum of 95% of each of the surface being tested shall be exhibit good wetting. The balance of the surface may contain only small pin holes, dewetted areas, and rough spots provided such defects are not concentrated in one area. There shall be no nonwetting or exposed base metal within the evaluated area.
Moisture resistance	AEC-Q200	MIL-STD-202 Method 106, t = 24 h/cycle Note: Steps 7a and 7b not required Unpowered Measurement at 24 ±4 h after test conclusion	No visible damage $ \Delta C/C_0 \leq 5\%$ $ \Delta \tan \delta \leq 0.005$ $R_{ins} \geq 50\%$ of initial limit
Electrical characterization	AEC-Q200	Temp A: -40 °C, Temp B: +110 °C, Temp C: +25 °C	No visible damage Summary to show min., max., mean and standard deviation at room as well as min. and max. operating temperatures
Terminal strength (leaded)	AEC-Q200	MIL-STD-202 Method 211, Test leaded device lead integrity only Conditions: A (2.27 kg), C (227 g), E (1.45 kg-mm)	No visible damage $ \Delta C/C_0 \leq 2\%$ $ \Delta \tan \delta \leq 0.0015$ $R_{ins} \geq 50\%$ of initial limit
Resistance to solvent	AEC-Q200	MIL-STD-202 Method 215, Notes: Also aqueous wash chemical - OKEM clean or equivalent Do not use banned solvents	No visible damage $ \Delta C/C_0 \leq 2\%$ $ \Delta \tan \delta \leq 0.0015$ $R_{ins} \geq 50\%$ of initial limit
Mechanical shock	AEC-Q200	MIL-STD-202 method 213, Figure 1 of method 213 Condition C	No visible damage $ \Delta C/C_0 \leq 3\%$ $ \Delta \tan \delta \leq 0.004$ $R_{ins} \geq 50\%$ of initial limit
Vibration	AEC-Q200	5 g's for 20 min, 12 cycles each of 3 orientations Use 8"X5" PCB, .031" thick. 7 secure points on one 8" side and 2 secure points at corners of opposite sides Parts mounted within 2" from any secure point. Test from 10-2000 Hz	No visible damage

Soldering

Solderability of leads

The solderability of terminal leads is tested to IEC 60068-2-20, test Ta, method 1.

Before a solderability test is carried out, terminals are subjected to accelerated ageing (to IEC 60068-2-2, test Ba: 4 h exposure to dry heat at 155 °C). Since the ageing temperature is far higher than the upper category temperature of the capacitors, the terminal wires should be cut off from the capacitor before the ageing procedure to prevent the solderability being impaired by the products of any capacitor decomposition that might occur.

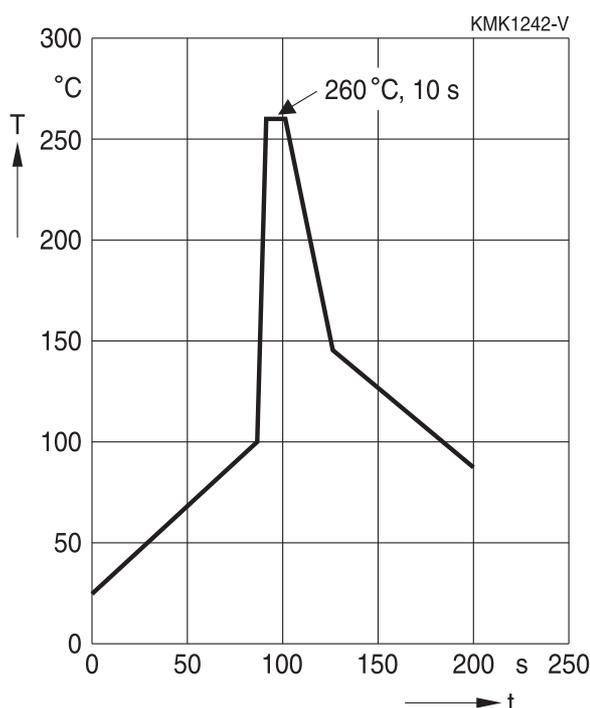
Solder bath temperature	245 ±3 °C
Soldering time	3.0 ±0.3 s
Immersion depth	2.0 +0/-0.5 mm from capacitor body or seating plane
Evaluation criteria: Visual inspection	Wetting of wire surface by new solder 95%, free-flowing solder

Resistance to soldering heat

Resistance to soldering heat is tested to IEC 60068-2-20, test Tb, method 1.

Conditions:

Series	Solder bath temperature	Soldering time
MKT boxed (except 2.5 × 6.5 × 7.2 mm) coated uncoated (lead spacing >10 mm)	260 ±5 °C	10 ±1 s
MFP MKP (lead spacing >7.5 mm)		
MKT boxed (case 2.5 × 6.5 × 7.2 mm)		5 ±1 s
MKT (lead spacing ≤7.5 mm)		<4 s recommended soldering profile for MKT uncoated (lead spacing ≤ 10 mm) and insulated (B32559)
MKT uncoated (lead spacing ≤10 mm) insulated (B32559)		



Immersion depth	2.0 +0/-0.5 mm from capacitor body or seating plane
Shield	Heat-absorbing board, (1.5 ±0.5) mm thick, between capacitor body and liquid solder
Evaluation criteria:	
Visual inspection	No visible damage
$\Delta C/C_0$	2% for MKT/MKP/MFP 5% for EMI suppression capacitors
$\tan \delta$	As specified in sectional specification

General notes on soldering

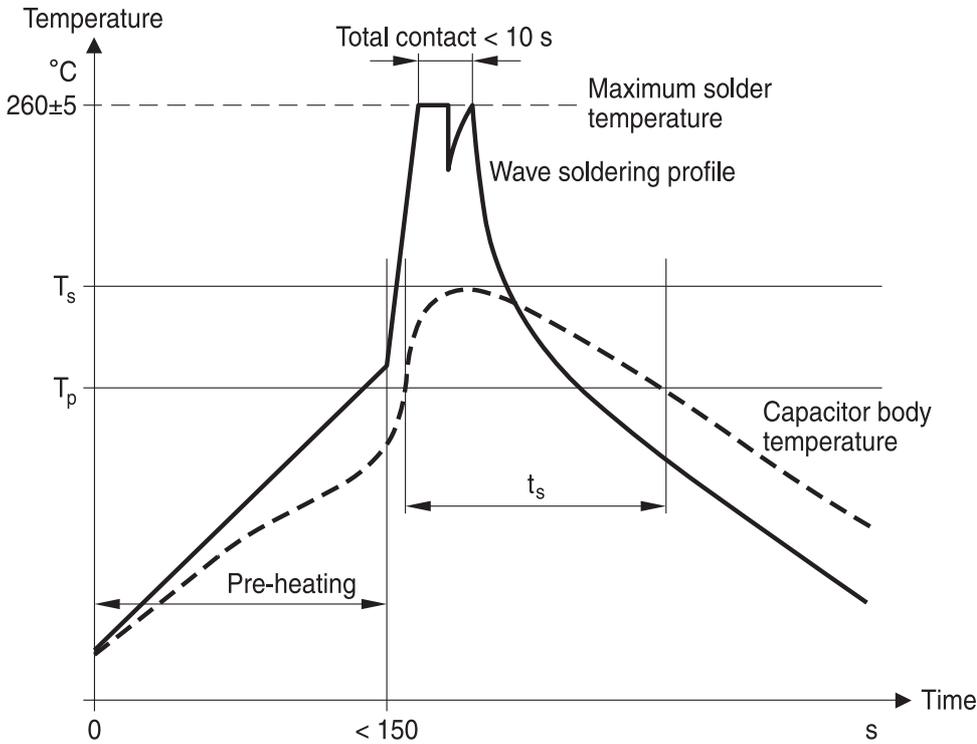
Permissible heat exposure loads on film capacitors are primarily characterized by the upper category temperature T_{max} . Long exposure to temperatures above this type-related temperature limit can lead to changes in the plastic dielectric and thus change irreversibly a capacitor's electrical characteristics. For short exposures (as in practical soldering processes) the heat load (and thus the possible effects on a capacitor) will also depend on other factors like:

- Pre-heating temperature and time
- Forced cooling immediately after soldering
- Terminal characteristics:
diameter, length, thermal resistance, special configurations (e.g. crimping)
- Height of capacitor above solder bath
- Shadowing by neighboring components
- Additional heating due to heat dissipation by neighboring components
- Use of solder-resist coatings

The overheating associated with some of these factors can usually be reduced by suitable countermeasures. For example, if a pre-heating step cannot be avoided, an additional or reinforced cooling process may possibly have to be included.

Recommendations

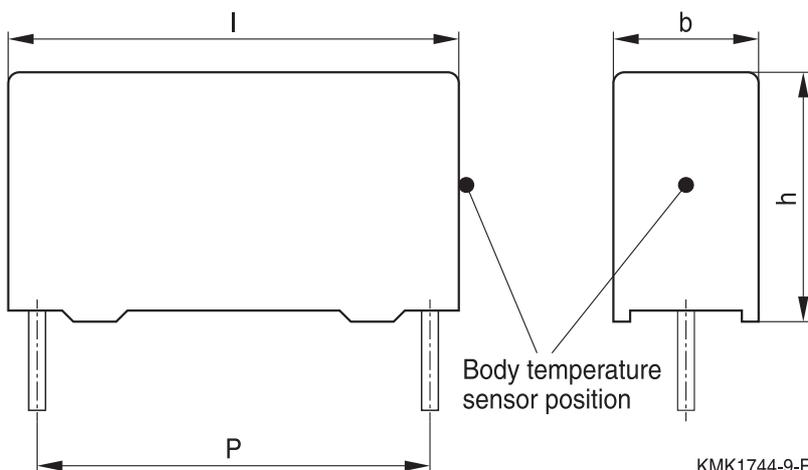
As a reference, the recommended wave soldering profile for our film capacitors is as follows:



T_s : Capacitor body maximum temperature at wave soldering

T_p : Capacitor body maximum temperature at pre-heating

KMK1745-A-E



KMK1744-9-E

Body temperature should follow the description below:

- MKP capacitor
 - During pre-heating: $T_p \leq 110 \text{ }^\circ\text{C}$
 - During soldering: $T_s \leq 120 \text{ }^\circ\text{C}$, $t_s \leq 45 \text{ s}$
- MKT capacitor
 - During pre-heating: $T_p \leq 125 \text{ }^\circ\text{C}$
 - During soldering: $T_s \leq 160 \text{ }^\circ\text{C}$, $t_s \leq 45 \text{ s}$

When SMD components are used together with leaded ones, the film capacitors should not pass into the SMD adhesive curing oven. The leaded components should be assembled after the SMD curing step.

Leaded film capacitors are not suitable for reflow soldering.

In order to ensure proper conditions for manual or selective soldering, the body temperature of the capacitor (T_s) must be $\leq 120 \text{ }^\circ\text{C}$.

One recommended condition for manual soldering is that the tip of the soldering iron should be $< 360 \text{ }^\circ\text{C}$ and the soldering contact time should be no longer than 3 seconds.

For uncoated MKT capacitors with lead spacings $\leq 10 \text{ mm}$ (B32560/B32561) the following measures are recommended:

- pre-heating to not more than $110 \text{ }^\circ\text{C}$ in the preheater phase
- rapid cooling after soldering

Please refer to our Film Capacitors Data Book in case more details are needed.

Cleaning

To determine whether the following solvents, often used to remove flux residues and other substances, are suitable for the capacitors described, refer to the table below:

Type	Ethanol, isopropanol, n-propanol	n-propanol-water mixtures, water with surface tension-reducing tensides (neutral)
MKT (uncoated)	Suitable	Unsuitable
MKT, MKP, MFP (coated/boxed)		Suitable

Even when suitable solvents are used, a reversible change of the electrical characteristics may occur in uncoated capacitors immediately after they are washed. Thus it is always recommended to dry the components (e.g. 4 h at 70 °C) before they are subjected to subsequent electrical testing.

Caution:

Consult us first if you wish to use new solvents!

Embedding of capacitors in finished assemblies

In many applications, finished circuit assemblies are embedded in plastic resins. In this case, both chemical and thermal influences of the embedding ("potting") and curing processes must be taken into account.

Our experience has shown that the following potting materials can be recommended: non-flexible epoxy resins with acid-anhydride hardeners; chemically inert, non-conducting fillers; maximum curing temperature of 100 °C.

Caution:

Consult us first if you wish to embed uncoated types!

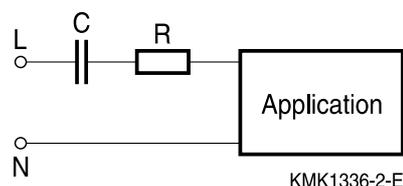
Application note for the different possible X1 / X2 positions

In series with the powerline (i.e. capacitive power supply)

Typical Applications:

- Power meters
- ECUs for white goods and household appliances
- Different sensor applications
- Severe ambient conditions

Basic circuit



Required features

- High capacitance stability over the lifetime
- Narrow tolerances for a controlled current supply

Recommended product series

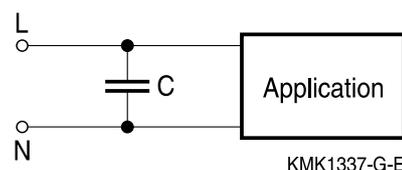
- B3293* (305 V AC) heavy duty with EN approval for X2 (UL Q1/2010)
- B3265* MKP series
standard MKP capacitor without safety approvals
- B3267*L MKP series
standard MKP capacitor without safety approvals
- B3292*H/J (305 V AC), severe ambient condition, approved as X2

In parallel with the powerline

Typical Applications:

Standard X2 are used parallel over the mains for reducing electromagnetic interferences coming from the grid. For such purposes they must meet the applicable EMC directives and standards.

Basic circuit



Required features

- Standard safety approvals (ENEC, UL, CSA, CQC)
- High pulse load capability
- Withstand surge voltages

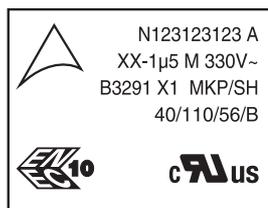
Recommended product series

- B3292*C/D (305 V AC)
standard series, approved as X2
- B3291* (330 V AC), approved as X1
- B3291* (530 V AC), approved as X1
- B3291* (550 V AC), approved as X1
- B3292*H/J (305 V AC), severe ambient condition, approved as X2

Capacitor markings

Depending on the capacitor size, the markings are positioned either on the side and/or the top of the component. The coded forms specified in IEC 60062:2004 are used to indicate the rated capacitance, capacitance tolerance and date of manufacture.

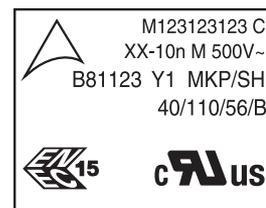
The lot number (production batch number) ensures unique identification of a particular capacitor and allows, together with the date of manufacture, exact assignment to the process data of the entire production run (traceability).

EMI suppression capacitors marking:
X1-330 V AC


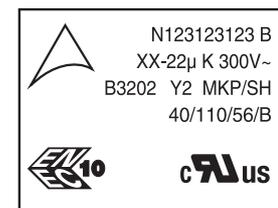
KMK1736-1

X1-530 V AC


KMK1737-2

Y1-500 V AC


KMK1557-M

Y2-300 V AC


KMK1738-3

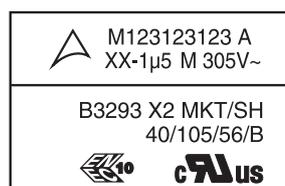
X2-305 V AC (B3292 C/D): For X2 EMI capacitors we distinguish between two different types of marking, depending on the capacitance.

C ≤ 10 µF


KMK2642-Z

C > 10 µF


KMK2643-U

X2-305 V AC (B3293 A/B):


KMK2644-I

X2-305 V AC (B3293 H/J):


KMK1582-Y

X2-350 V AC


KMK1872-T

For all EMI capacitors:

If the capacitor is wide enough, the entire marking will be on the top. In this case, the stamping will contain the following information:

1st line: Manufacturer's logo, lot number, revision status

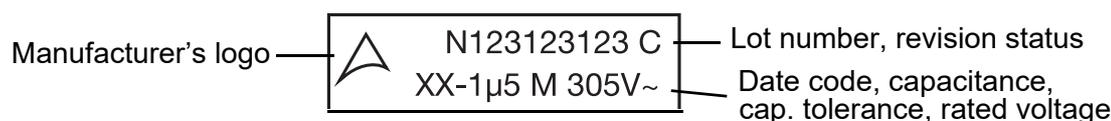
2nd line: Date code, capacitance, cap. tolerance, rated voltage

3rd line: Type number, interference suppression, sub class, style/self-healing

4th line: Climatic category

5th line: Marks of conformity

If the capacitor is not wide enough for the entire marking, the information in the marking will be split between the top and side. In this case, the following partial information will be found on the top:



Codes for rated capacitance

Rated capacitance	To IEC 60062	Short code
100 pF	100p	n1
150 pF	150p	n15
1.0 nF	1n0	1n
1.5 nF	1n5	
10 nF	10n	
100 nF	100n	μ1
150 nF	150n	μ15
1.0 μF	1μ0	1μ
1.5 μF	1μ5	
10 μF	10μ	
10 μF	15μ	

Codes for capacitance tolerance

Capacitance tolerance	Code letter	Remark
	A	Capacitance tolerances for which no code letter is defined can be indicated by an A. The meaning of code A must then be mutually specified in other documentation.
±2.5%	H	
±5%	J	
±10%	K	
±20%	M	

Codes for date of manufacture (to IEC 60062:2016)

Code for year				Code for month			
Year	Code letter	Year	Code letter	Month	Code numeral	Month	Code numeral/letter
2018	K	2024	S	January	1	July	7
2019	L	2025	T	February	2	August	8
2020	M	2026	U	March	3	September	9
2021	N	2027	V	April	4	October	O
2022	P	2028	W	May	5	November	N
2023	R	2029	X	June	6	December	D

E.g.: R5 2023 May

Marking types

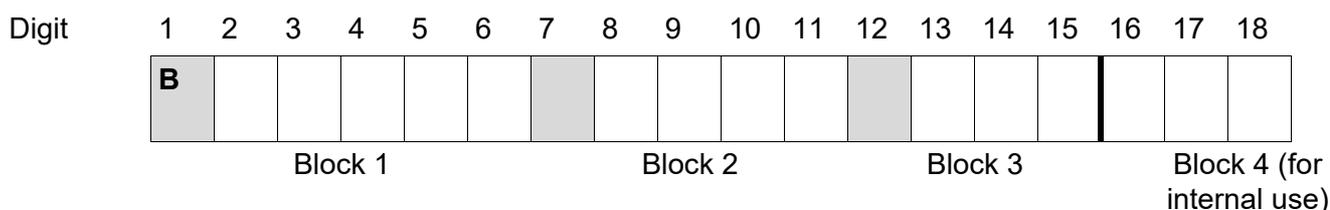
The capacitors may have either an ink-jet marking or a laser marking. The main advantage of laser marking is that it cannot be removed by solvents, which ensures the reliable identification of the capacitor. Moreover, because the laser marking process reduces the amount of chemicals used, it is an environmentally friendly marking solution.

Ordering code system

A component and the packing in which it is to be delivered are defined by the ordering code, which has 15 digits (plus 3 additional digits for internal use). For all capacitors the ordering codes are explicitly stated (together with the corresponding tolerance and/or packing variants) in the data sheets.

Should there be any doubt about the coding system, however, then it is better to order the capacitor using a plain text description (i.e. without a code).

Basic structure of the ordering code:



Digit	Meaning																					
1	B = Passive components																					
2,3	32 = Metallized film capacitors, EMI suppression capacitors 81 = EMI suppression capacitors																					
4 ... 6	Type (block 1 is termed the "type number")																					
7	Revision status																					
8	Rated DC voltage, coded (not for EMI suppression capacitors)																					
9 ... 11	Rated capacitance (coding method for value in pF) Examples:																					
	<table style="margin-left: 40px;"> <tr> <td style="text-align: center;">Digit</td> <td style="text-align: center;">9</td> <td style="text-align: center;">10</td> <td style="text-align: center;">11</td> <td></td> <td></td> <td></td> </tr> <tr> <td></td> <td style="border: 1px solid black; text-align: center;">1</td> <td style="border: 1px solid black; text-align: center;">5</td> <td style="border: 1px solid black; text-align: center;">4</td> <td style="text-align: center;">K</td> <td style="text-align: center;">=</td> <td style="text-align: center;">15</td> </tr> <tr> <td></td> <td colspan="3" style="border: 1px solid black; text-align: center;">154</td> <td></td> <td></td> <td style="text-align: center;">• 10⁴ pF = 150 nF</td> </tr> </table> <p style="margin-left: 40px;">B 3 2 6 5 2 A 3</p>	Digit	9	10	11					1	5	4	K	=	15		154					• 10 ⁴ pF = 150 nF
Digit	9	10	11																			
	1	5	4	K	=	15																
	154					• 10 ⁴ pF = 150 nF																
12	Code letter for capacitance tolerance																					
13 ... 15	Codes for lead and taping parameters (refer to respective data sheet)																					
16 ... 18	Internal use																					

Cautions and warnings

- Do not exceed the upper category temperature (UCT).
- Do not apply any mechanical stress to the capacitor terminals.
- Avoid any compressive, tensile or flexural stress.
- Do not move the capacitor after it has been soldered to the PC board.
- Do not pick up the PC board by the soldered capacitor.
- Do not place the capacitor on a PC board whose PTH hole spacing differs from the specified lead spacing.
- Do not exceed the specified time or temperature limits during soldering.
- Avoid external energy inputs, such as fire or electricity.
- Avoid overload of the capacitors.
- Consult us if application is with severe temperature and humidity condition.
- There are no serviceable or repairable parts inside the capacitor. Opening the capacitor or any attempts to open or repair the capacitor will void the warranty and liability of TDK Electronics.
- Please note that the standards referred to in this publication may have been revised in the meantime.

The table below summarizes the safety instructions that must always be observed. A detailed description can be found in the relevant sections of the chapters “General technical information” and “Mounting guidelines”.

Topic	Safety information	Reference chapter “General technical information”
Storage conditions	Make sure that capacitors are stored within the specified range of time, temperature and humidity conditions.	4.5 “Storage conditions” **
Flammability	Avoid external energy, such as fire or electricity (passive flammability), avoid overload of the capacitors (active flammability) and consider the flammability of materials.	5.3 “Flammability”
Resistance to vibration	Do not exceed the tested ability to withstand vibration. The capacitors are tested to IEC 60068-2-6:2007. TDK Electronics offers film capacitors specially designed for operation under more severe vibration regimes such as those found in automotive applications. Consult our catalog “Film Capacitors for Automotive Electronics”.	5.2 “Resistance to vibration”
Soldering	Do not exceed the specified time or temperature limits during soldering.	1 “Soldering”
Cleaning	Use only suitable solvents for cleaning capacitors.	2 “Cleaning”
Embedding of capacitors in finished assemblies	When embedding finished circuit assemblies in plastic resins, chemical and thermal influences must be taken into account. Caution: Consult us first, if you also wish to embed other uncoated component types!	3 “Embedding of capacitors in finished assemblies”

** Maximum duration 2 years

Design of our capacitors

Our EMI capacitors use polypropylene (PP) film metalized with a thin layer of Zinc (Zn). The following key points have made this design suitable to IEC/UL testing, holding a minimum size.

- Overvoltage AC capability with very high temperature Endurance test of IEC 60384-14:2013 (4th edition) / UL 60384-14:2014 (2nd edition) must be performed at $1.25 \times V_R$ at maximum temperature, during 1000 hours, with a capacitance drift less than 10%.
- Higher breakdown voltage withstanding if compared to other film metallizations, like Aluminum. IEC 60384-14:2013 (4th edition) / UL 60384-14:2014 (2nd edition) establishes high voltage tests performed at $4.3 \times V_R - 1$ minute, impulse testing at 2500 V for $C = 1 \mu\text{F}$ and active flammability tests.
- Damp heat steady state: 40 °C/ 93% RH / 56 days. (without voltage or current load)

Effect of humidity on capacitance stability

Long contact of a film capacitor with humidity can produce irreversible effects. Direct contact with liquid water or excess exposure to high ambient humidity or dew will eventually remove the film metallization and thus destroy the capacitor. Plastic boxed capacitors must be properly tested in the final application at the worst expected conditions of temperature and humidity in order to check if any parameter drift may provoke a circuit malfunction.

In case of penetration of humidity through the film, the layer of Zinc can be degraded, specially under AC operation (change of polarity), accelerated by the temperature, provoking an increment of the serial resistance of the electrode and eventually a reduction of the capacitance value.

For DC operation, the parameter drift is much less.

Plastic boxes and resins can not protect 100% against humidity. Metal enclosures, resin potting or coatings or similar measures by customers in their applications will offer additional protection against humidity penetration.

Display of ordering codes for TDK Electronics products

The ordering code for one and the same product can be represented differently in data sheets, data books, other publications, on the company website, or in order-related documents such as shipping notes, order confirmations and product labels. The varying representations of the ordering codes are due to different processes employed and do not affect the specifications of the respective products.

Detailed information can be found on the Internet under www.tdk-electronics.tdk.com/orderingcodes.

Correlation of data sheet values and modelling tool outputs

Data sheet values and results of design tools may deviate as they have not been derived in the same context.

While data sheets show individual parameter statements without considering a possible dependency to other parameters. Tools model a complete given scenario as input and processed inside the tool.

Furthermore as we constantly strive to improve our models, the results of tools can change over time and be a non-binding indication only.

Symbols and terms

Symbol	English	German
α	Heat transfer coefficient	Wärmeübergangszahl
α_C	Temperature coefficient of capacitance	Temperaturkoeffizient der Kapazität
A	Capacitor surface area	Kondensatoroberfläche
β_C	Humidity coefficient of capacitance	Feuchtekoeffizient der Kapazität
C	Capacitance	Kapazität
C_R	Rated capacitance	Nennkapazität
ΔC	Absolute capacitance change	Absolute Kapazitätsänderung
$\Delta C/C$	Relative capacitance change (relative deviation of actual value)	Relative Kapazitätsänderung (relative Abweichung vom Ist-Wert)
$\Delta C/C_R$	Capacitance tolerance (relative deviation from rated capacitance)	Kapazitätstoleranz (relative Abweichung vom Nennwert)
dt	Time differential	Differentielle Zeit
Δt	Time interval	Zeitintervall
ΔT	Absolute temperature change (self-heating)	Absolute Temperaturänderung (Selbsterwärmung)
$\Delta \tan \delta$	Absolute change of dissipation factor	Absolute Änderung des Verlustfaktors
ΔV	Absolute voltage change	Absolute Spannungsänderung
dV/dt	Time differential of voltage function (rate of voltage rise)	Differentielle Spannungsänderung (Spannungsflankensteilheit)
$\Delta V/\Delta t$	Voltage change per time interval	Spannungsänderung pro Zeitintervall
E	Activation energy for diffusion	Aktivierungsenergie zur Diffusion
ESL	Self-inductance	Eigeninduktivität
ESR	Equivalent series resistance	Ersatz-Serienwiderstand
f	Frequency	Frequenz
f_1	Frequency limit for reducing permissible AC voltage due to thermal limits	Grenzfrequenz für thermisch bedingte Reduzierung der zulässigen Wechselspannung
f_2	Frequency limit for reducing permissible AC voltage due to current limit	Grenzfrequenz für strombedingte Reduzierung der zulässigen Wechselspannung
f_r	Resonant frequency	Resonanzfrequenz
F_D	Thermal acceleration factor for diffusion	Therm. Beschleunigungsfaktor zur Diffusion
F_T	Derating factor	Deratingfaktor
i	Current (peak)	Stromspitze
I_C	Category current (max. continuous current)	Kategoriestrom (max. Dauerstrom)
I_{RMS}	(Sinusoidal) alternating current, root-mean- square value	(Sinusförmiger) Wechselstrom
i_z	Capacitance drift	Inkonstanz der Kapazität
k_0	Pulse characteristic	Impuls Kennwert
L_S	Series inductance	Serieninduktivität
λ	Failure rate	Ausfallrate

Symbol	English	German
λ_0	Constant failure rate during useful service life	Konstante Ausfallrate in der Nutzungsphase
λ_{test}	Failure rate, determined by tests	Experimentell ermittelte Ausfallrate
P_{diss}	Dissipated power	Abgegebene Verlustleistung
P_{gen}	Generated power	Erzeugte Verlustleistung
Q	Heat energy	Wärmeenergie
ρ	Density of water vapor in air	Dichte von Wasserdampf in Luft
R	Universal molar constant for gases	Allg. Molarkonstante für Gas
R	Ohmic resistance of discharge circuit	Ohmscher Widerstand des Entladekreises
R_i	Internal resistance	Innenwiderstand
R_{ins}	Insulation resistance	Isolationswiderstand
R_p	Parallel resistance	Parallelwiderstand
R_s	Series resistance	Serienwiderstand
S	severity (humidity test)	Schärfegrad (Feuchtetest)
t	Time	Zeit
T	Temperature	Temperatur
τ	Time constant	Zeitkonstante
$\tan \delta$	Dissipation factor	Verlustfaktor
$\tan \delta_D$	Dielectric component of dissipation factor	Dielektrischer Anteil des Verlustfaktors
$\tan \delta_p$	Parallel component of dissipation factor	Parallelanteil des Verlustfaktors
$\tan \delta_s$	Series component of dissipation factor	Serienanteil des Verlustfaktors
T_A	Temperature of the air surrounding the component	Temperatur der Luft, die das Bauteil umgibt
T_{max}	Upper category temperature	Obere Kategorietemperatur
T_{min}	Lower category temperature	Untere Kategorietemperatur
t_{OL}	Operating life at operating temperature and voltage	Betriebszeit bei Betriebstemperatur und -spannung
T_{op}	Operating temperature, $T_A + \Delta T$	Betriebstemperatur, $T_A + \Delta T$
T_R	Rated temperature	Nenntemperatur
T_{ref}	Reference temperature	Referenztemperatur
t_{SL}	Reference service life	Referenz-Lebensdauer
V_{AC}	AC voltage	Wechselspannung
V_C	Category voltage	Kategoriespannung
$V_{\text{C,RMS}}$	Category AC voltage	(Sinusförmige) Kategorie-Wechselspannung
V_{CD}	Corona-discharge onset voltage	Teilentlade-Einsatzspannung
V_{ch}	Charging voltage	Ladespannung
V_{DC}	DC voltage	Gleichspannung
V_{FB}	Fly-back capacitor voltage	Spannung (Flyback)

Symbol	English	German
V_i	Input voltage	Eingangsspannung
V_o	Output voltage	Ausgangssspannung
V_{op}	Operating voltage	Betriebsspannung
V_p	Peak pulse voltage	Impuls-Spitzenspannung
V_{pp}	Peak-to-peak voltage Impedance	Spannungshub
V_R	Rated voltage	Nennspannung
\hat{V}_R	Amplitude of rated AC voltage	Amplitude der Nenn-Wechselspannung
V_{RMS}	(Sinusoidal) alternating voltage, root-mean-square value	(Sinusförmige) Wechselspannung
V_{SC}	S-correction voltage	Spannung bei Anwendung "S-correction"
V_{sn}	Snubber capacitor voltage	Spannung bei Anwendung "Beschaltung"
Z	Impedance	Scheinwiderstand
e	Lead spacing	Rastermaß

The following applies to all products named in this publication:

- 1 Some parts of this publication contain **statements about the suitability of our products for certain areas of application**. These statements are based on our knowledge of typical requirements that are often placed on our products in the areas of application concerned. We nevertheless expressly point out **that such statements cannot be regarded as binding statements about the suitability of our products for a particular customer application**. As a rule we are either unfamiliar with individual customer applications or less familiar with them than the customers themselves. For these reasons, it is always ultimately incumbent on the customer to check and decide whether a product with the properties described in the product specification is suitable for use in a particular customer application.
- 2 We also point out that **in individual cases, a malfunction of electronic components or failure before the end of their usual service life cannot be completely ruled out in the current state of the art, even if they are operated as specified**. In customer applications requiring a very high level of operational safety and especially in customer applications in which the malfunction or failure of an electronic component could endanger human life or health (e.g. in accident prevention or life-saving systems), it must therefore be ensured by means of suitable design of the customer application or other action taken by the customer (e.g. installation of protective circuitry or redundancy) that no injury or damage is sustained by third parties in the event of malfunction or failure of an electronic component.
- 3 **The warnings, cautions and product-specific notes must be observed.**
- 4 In order to satisfy certain technical requirements, **some of the products described in this publication may contain substances subject to restrictions in certain jurisdictions (e.g. because they are classed as hazardous)**. Useful information on this will be found in our Material Data Sheets on the Internet (www.tdk-electronics.tdk.com/material). Should you have any more detailed questions, please contact our sales offices.
- 5 We constantly strive to improve our products. Consequently, **the products described in this publication may change from time to time**. The same is true of the corresponding product specifications. Please check therefore to what extent product descriptions and specifications contained in this publication are still applicable before or when you place an order.
We also **reserve the right to discontinue production and delivery of products**. Consequently, we cannot guarantee that all products named in this publication will always be available. The aforementioned does not apply in the case of individual agreements deviating from the foregoing for customer-specific products.
- 6 Unless otherwise agreed in individual contracts, **all orders are subject to our General Terms and Conditions of Supply**.
- 7 **Our manufacturing sites serving the automotive business apply the IATF 16949 standard**. The IATF certifications confirm our compliance with requirements regarding the quality management system in the automotive industry. Referring to customer requirements and customer specific requirements ("CSR") TDK always has and will continue to have the policy of respecting individual agreements. Even if IATF 16949 may appear to support the acceptance of unilateral requirements, we hereby like to emphasize that **only requirements mutually agreed upon can and will be implemented in our Quality Management System**. For clarification purposes we like to point out that obligations from IATF 16949 shall only become legally binding if individually agreed upon.

Important notes

- 8 The trade names EPCOS, CarXield, CeraCharge, CeraDiode, CeraLink, CeraPad, CeraPlas, CSMP, CTVS, DeltaCap, DigiSiMic, FilterCap, FormFit, InsuGate, LeaXield, MediPlas, MiniBlue, MiniCell, MKD, MKK, ModCap, MotorCap, PCC, PhaseCap, PhaseCube, PhaseMod, PhiCap, PiezoBrush, PlasmaBrush, PowerHap, PQSine, PQvar, SIFERRIT, SIFI, SIKOREL, SilverCap, SIMDAD, SiMic, SIMID, SineFormer, SIOV, SurfIND, ThermoFuse, WindCap, XieldCap are **trademarks registered or pending** in Europe and in other countries. Further information will be found on the Internet at www.tdk-electronics.tdk.com/trademarks.

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