

NL3S588

USB 2.0-Capable Ultra-Low THD DPDT Switch

The NL3S588 is a single supply, bidirectional, double-pole/double-throw (DPDT) switch suitable for both hi-fidelity audio and high-speed data applications.

The NL3S588 features ultra-low distortion, high OFF-Isolation analog switches that can pass analog signals that are positive and negative with respect to ground. It is targeted at consumer and professional DC-coupled GND-referenced audio switching applications such as computer sound cards and home theater products.

The NL3S588 may also be used in high-speed differential data routing applications. Both channels are USB 2.0-compliant.

Features

- DPDT Switch
- 3.3 V Single Supply Operation
- Available in 1.4 mm x 1.8 mm UQFN10
- This Device is Pb-Free, Halogen Free/BFR Free and RoHS Compliant

Audio Capabilities

- 2 V_{RMS} Signal Switching
- -116 dB THD+N into 20 k Ω Load at 2 V_{RMS}
- -112 dB THD+N into 32 Ω Load at 0.707 V_{RMS}
- Signal to Noise Ratio: > 125 dBV
- ± 0.004 dB Insertion Loss at 1 kHz, 20 k Ω Load
- ± 0.0008 dB Gain Variation 20 Hz to 20 kHz
- 112 dB Signal Muting into 20 k Ω Load
- 131 dB PSRR 20 Hz to 20 kHz

High-Speed Data Capabilities

- Input Signal Range: 0 V to V_{DD}
- C_{ON} : 8.9 pF (Typ)
- Data Rate: USB 2.0-Compliant – up to 480 Mbps
- Bandwidth: 580 MHz

Applications

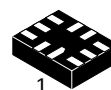
- Hi-Fi Audio Switching
- USB 2.0 High-Speed Data Switching
- USB 3.x Type C Switching



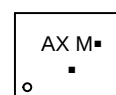
ON Semiconductor®

www.onsemi.com

MARKING DIAGRAM



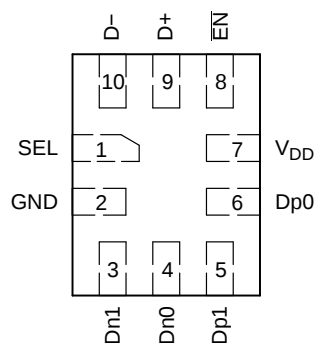
UQFN10
MU SUFFIX
CASE 488AT



AX = Device Code
M = Date Code
▪ = Pb-Free Device

(Note: Microdot may be in either location)

PIN ASSIGNMENT



(Top View)

ORDERING INFORMATION

Device	Package	Shipping†
NL3S588MUTBG	UQFN10 (Pb-Free)	3000 / Tape & Reel

†For information on tape and reel specifications, including part orientation and tape sizes, please refer to our Tape and Reel Packaging Specification Brochure, BRD8011/D.

NL3S588

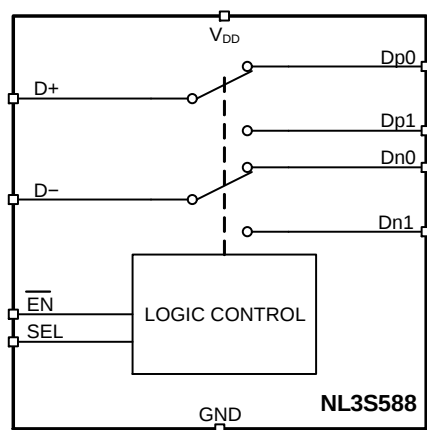


Figure 1. Block Diagram

FUNCTION TABLE

INPUTS		Operating Mode
EN	SEL	
0	0	Dp0 connected to D+ / Dn0 connected to D-
0	1	Dp1 connected to D+ / Dn1 connected to D-
1	X	Shutdown (I/Os Disconnected)

NOTE: EN Logic "0" ≤ 0.5 V, Logic "1" ≥ 1.4 V or float.

SEL Logic "0" ≤ 0.5 V, Logic "1" ≥ 1.4 V.

X = Don't Care

PIN DESCRIPTIONS

PIN NAME	PIN	DESCRIPTION
SEL	1	Channel Select
GND	2	Ground
Dn1	3	Normally-Open I/O
Dp1	5	
Dn0	4	Normally-Closed I/O
Dp0	6	
V _{DD}	7	System power supply pin (+3 V to +3.6 V)
EN	8	Signal mute control pin
D+	9	Common I/O
D-	10	

MAXIMUM RATINGS

Symbol	Rating	Value	Unit
V_{DD}	Positive 3 V DC Supply Voltage	-0.5 to +4.1	V
V_{IS}	Analog Input/Output Voltage (D+, D-, Dpx, Dnx)	-3.1 to $V_{DD} + 0.5$	V
V_{IN}	Digital Input Voltage ($\overline{EN}$, SEL)	-0.5 to $V_{DD} + 0.5$	V
I_{IO}	Switch Continuous Current (D+, D-, Dpx, Dnx)	± 300	mA
I_{IO_PK}	Switch Peak Current (D+, D-, Dpx, Dnx) (Pulsed 1 ms, 10% Duty Cycle, Max).	± 500	mA
P_D	Power Dissipation in Still Air	800	mW
T_s	Storage Temperature	-65 to +150	°C
T_L	Lead Temperature, 1 mm from Case for 10 seconds	260	°C
T_J	Junction Bias Under Bias	150	°C
θ_{JA}	Thermal Resistance	80	°C/W
T_s	Storage Temperature	-65 to +150	°C
MSL	Moisture Sensitivity	Level 1	
F_R	Flammability Rating	Oxygen Index: 30% – 35% UL94-V0 (0.125 in)	°C
ESD	ESD Protection	Human Body Model Machine Model	3000 200
I_L	Latch-up Current, Above V_{CC} and below GND at 125°C (Note 1)	± 300	mA

Stresses exceeding those listed in the Maximum Ratings table may damage the device. If any of these limits are exceeded, device functionality should not be assumed, damage may occur and reliability may be affected.

1. Tested to EIA/JESD78.

RECOMMENDED OPERATING CONDITIONS

Symbol	Parameter	Min	Max	Unit
V_{DD}	Positive DC Supply Voltage	3.0	3.6	V
V_S	Switch Input / Output Voltage (D+, D-, Dpx, Dnx)	-2.9	V_{DD}	V
V_{IN}	Digital Select Input Voltage ($\overline{EN}$, SEL)	GND	V_{DD}	V
T_A	Operating Temperature Range	-40	+85	°C

DC ELECTRICAL CHARACTERISTICS (Voltages referenced to GND): $V_{DD} = +3.0$ V to +3.6 V, GND = 0 V, $V_S = 2 V_{RMS}$, $R_{LOAD} = 20$ k Ω , $f = 1$ kHz, $V_{SELH} = V_{ENH} = 1.4$ V, $V_{SELL} = V_{ENL} = 0.5$ V, (Note 2), Unless otherwise specified.

Parameter	Test Conditions	Supply (V)	Temp (°C)	Min (Notes 3, 4)	Typ	Max (Notes 3, 4)	Units
-----------	-----------------	------------	-----------	------------------	-----	------------------	-------

ANALOG SWITCH CHARACTERISTICS

Analog Signal Range, V_{ANALOG}		3.3	Full	–	2	–	V_{RMS}
ON-Resistance, r_{ON}	I_{D+} or $I_{D-} = 80$ mA, V_{Dpx} or $V_{Dnx} = -2.828$ V to +2.828 V (See Figure 5)	3.3	25	–	2.1	–	Ω
			Full	–	2.5	–	
r_{ON} Matching Between Channels, Δr_{ON}	I_{D+} or $I_{D-} = 80$ mA, V_{Dpx} or $V_{Dnx} =$ Voltage at max r_{ON} over -2.828 V to +2.828 V (Note 7)	3.3	25	–	0.046	–	Ω
			Full	–	0.23	–	
r_{ON} Flatness, $r_{FLAT(ON)}$	I_{D+} or $I_{D-} = 80$ mA, V_{Dpx} or $V_{Dnx} = -2.828$ V, 0V, +2.828 V (Note 5)	3.3	25	–	0.047	0.05	Ω
			Full	–	0.092	–	

2. V_{IN} = input voltage to perform proper function.

3. The algebraic convention, whereby the most negative value is a minimum and the most positive a maximum, is used in this data sheet.

4. Parameters with MIN and/or MAX limits are 100% tested at +25°C, unless otherwise specified. Temperature limits established by characterization and are not production tested.

5. Flatness is defined as the difference between maximum and minimum value of ON-resistance at the specified analog signal voltage points.

6. Limits established by characterization and are not production tested.

7. r_{ON} matching between channels is calculated by subtracting the channel with the highest max r_{ON} value from the channel with lowest max r_{ON} value.

8. Crosstalk is inversely proportional to source impedance.

DC ELECTRICAL CHARACTERISTICS (Voltages referenced to GND): $V_{DD} = +3.0$ V to $+3.6$ V, $GND = 0$ V, $V_S = 2$ V_{RMS}, $R_{LOAD} = 20$ k Ω , $f = 1$ kHz, $V_{SELH} = V_{ENH} = 1.4$ V, $V_{SELL} = V_{ENL} = 0.5$ V, (Note 2), Unless otherwise specified.

Parameter	Test Conditions	Supply (V)	Temp (°C)	Min (Notes 3, 4)	Typ	Max (Notes 3, 4)	Units
-----------	-----------------	------------	-----------	------------------	-----	------------------	-------

ANALOG SWITCH CHARACTERISTICS

D+, D-, Dpx, Dnx Pull-down Resistance	V_{Dpx} or $V_{Dnx} = -2.83$ V, 2.83 V, V_{D+} or $V_{D-} = -2.83$ V, 2.83 V, $V_{EN} = 3.6$ V, measure current, calculate resistance.	3.6	25	225	300	375	k Ω
			Full	–	345	–	

DYNAMIC CHARACTERISTICS

THD+N	$V_S = 2$ V _{RMS} , $f = 1$ kHz, A-weighted filter, $R_{LOAD} = 20$ k Ω	3.3	25	–	< –116	–	dB
	$V_S = 1.9$ V _{RMS} , $f = 1$ kHz, A-weighted filter, $R_{LOAD} = 20$ k Ω		25	–	< –116	–	
	$V_S = 1.8$ V _{RMS} , $f = 1$ kHz, A-weighted filter, $R_{LOAD} = 20$ k Ω		25	–	< –116	–	
	$V_S = 0.707$ V _{RMS} , $f = 1$ kHz, A-weighted filter, $R_{LOAD} = 32$ Ω		25	–	< –112	–	
SNR	$f = 20$ Hz to 20 kHz, A-weighted filter, inputs grounded, $R_{LOAD} = 20$ k Ω or 32 Ω	3.3	25	–	> 125	–	dBV
Insertion Loss, G_{ON}	$f = 1$ kHz, $R_{LOAD} = 20$ k Ω	3.3	25	–	± 0.004	–	dB
Gain vs Frequency, G_f	$f = 20$ Hz to 20 kHz, $R_{LOAD} = 20$ k Ω , reference to G_{ON} at 1 kHz	3.3	25	–	± 0.0008	–	dB
Stereo Channel Imbalance Dp0 and Dn0, Dp1 and Dn1	$f = 20$ Hz to 20 kHz, $R_{LOAD} = 20$ k Ω	3.3	25	–	± 0.0001	–	dB
OFF–Isolation (Disabling)	$f = 20$ Hz to 22 kHz, $D+ = D- = 2$ V _{RMS} , $R_{LOAD} = 20$ k Ω , $SEL = "X"$	3.3	25	–	112	–	dB
	$f = 20$ Hz to 22 kHz, V_{D+} or $V_{D-} = 0.7$ V _{RMS} , $R_{LOAD} = 32$ Ω		25	–	129	–	
Crosstalk (Channel-to- Channel)	$R_L = 20$ k Ω , $f = 20$ Hz to 20 kHz, $V_S = 2$ V _{RMS} , signal source impedance = 20 Ω , (Note 8)	3.3	25	–	102	–	dB
	$R_L = 32$ Ω , $f = 20$ Hz to 20 kHz, $V_S = 0.7$ V _{RMS} , signal source impedance = 20 Ω , (Note 8)		25	–	129	–	
PSRR	$f = 1$ kHz, $V_S = 100$ mV _{RMS} , inputs grounded	3.3	25	–	131	–	dB
	$f = 20$ kHz, $V_S = 100$ mV _{RMS} , inputs grounded		25	–	133	–	
Bandwidth, –3 dB	$R_{LOAD} = 50$ Ω	3.3	25	–	580	–	MHz
ON to Disable Time, $T_{TRANS-OM}$		3.3	25	–	250	–	ns
Disable to ON Time, $T_{TRANS-MO}$	$V_{IS} = 1.5$ V	3.3	25	–	1680	–	μ s
Turn-ON Time, t_{ON}	V_{Dpx} or $V_{Dnx} = 1.5$ V, $V_{EN} = 0$ V, $R_L = 32$ Ω (See Figure 2)	3.3	25	–	14	–	μ s
Turn-OFF Time, t_{OFF}	V_{Dpx} or $V_{Dnx} = 1.5$ V, $V_{EN} = 0$ V, $R_L = 32$ Ω (See Figure 2)	3.3	25	–	95	–	ns

- V_{IN} = input voltage to perform proper function.
- The algebraic convention, whereby the most negative value is a minimum and the most positive a maximum, is used in this data sheet.
- Parameters with MIN and/or MAX limits are 100% tested at +25°C, unless otherwise specified. Temperature limits established by characterization and are not production tested.
- Flatness is defined as the difference between maximum and minimum value of ON-resistance at the specified analog signal voltage points.
- Limits established by characterization and are not production tested.
- r_{ON} matching between channels is calculated by subtracting the channel with the highest max r_{ON} value from the channel with lowest max r_{ON} value.
- Crosstalk is inversely proportional to source impedance.

DC ELECTRICAL CHARACTERISTICS (Voltages referenced to GND): $V_{DD} = +3.0\text{ V to }+3.6\text{ V}$, $GND = 0\text{ V}$, $V_S = 2\text{ V}_{RMS}$, $R_{LOAD} = 20\text{ k}\Omega$, $f = 1\text{ kHz}$, $V_{SELH} = V_{ENH} = 1.4\text{ V}$, $V_{SELL} = V_{ENL} = 0.5\text{ V}$, (Note 2), Unless otherwise specified.

Parameter	Test Conditions	Supply (V)	Temp (°C)	Min (Notes 3, 4)	Typ	Max (Notes 3, 4)	Units
DYNAMIC CHARACTERISTICS							
Break-Before-Make Time Delay, t_D	V_{Dpx} or $V_{Dnx} = 1.5\text{ V}$, $V_{EN} = 0\text{ V}$, $R_L = 32\text{ }\Omega$ (See Figure 3)	3.6	25	–	10	–	μs
OFF-Isolation	$R_L = 50\text{ }\Omega$, $f = 1\text{ MHz}$, V_{D+} or $V_{D-} = 1\text{ V}_{RMS}$ (See Figure 4)	3.3	25	–	70	–	dB
Crosstalk (Channel-to-Channel)	$R_L = 50\text{ }\Omega$, $f = 1\text{ MHz}$, V_{D+} or $V_{D-} = 1\text{ V}_{RMS}$ (See Figure 4)	3.3	25	–	89	–	dB
Dpx, Dnx OFF Capacitance, C_{OFF}	$f = 1\text{ MHz}$, V_{Dpx} or $V_{Dnx} = V_{D+}$ or $V_{D-} = 0\text{ V}$ (See Figure 7)	3.3	25	–	2.7	–	pF
D+, D- ON Capacitance, $C_{COM(ON)}$	$f = 1\text{ MHz}$, V_{Dpx} or $V_{Dnx} = V_{COM} = 0\text{ V}$ (See Figure 7)	3.3	25	–	8.9	–	pF
Differential Insertion Loss, D_{IL}	$f = 10\text{ MHz}$	3.3	25	–	–0.22	–	dB
	$f = 800\text{ MHz}$	3.3	25	–	–3.3	–	
Differential OFF-Isolation, D_{ISO}	$f = 10\text{ MHz}$	3.3	25	–	–44	–	dB
	$f = 800\text{ MHz}$	3.3	25	–	–16	–	
Differential Crosstalk, D_{CTK}	$f = 10\text{ MHz}$	3.3	25	–	–44	–	dB
	$f = 800\text{ MHz}$	3.3	25	–	–16	–	

POWER SUPPLY CHARACTERISTICS

Power Supply Range, V_{DD}		3.3	Full	3	–	3.6	V
Positive Supply Current, I_+	$V_{EN} = 0\text{ V}$, $V_{SEL} = 0\text{ V}$ or V_{DD}	3.6	25	–	54	65	μA
			Full	–	59	–	
	$V_{EN} = V_{DD}$, $V_{SEL} = 0\text{ V}$ or V_{DD}	3.6	25	–	14	40	μA
			Full	–	15	–	
	$V_{EN} = 0\text{ V}$, $V_{SEL} = 1.8\text{ V}$	3.6	25	–	55	65	μA
			Full	–	58	–	

- V_{IN} = input voltage to perform proper function.
- The algebraic convention, whereby the most negative value is a minimum and the most positive a maximum, is used in this data sheet.
- Parameters with MIN and/or MAX limits are 100% tested at +25°C, unless otherwise specified. Temperature limits established by characterization and are not production tested.
- Flatness is defined as the difference between maximum and minimum value of ON-resistance at the specified analog signal voltage points.
- Limits established by characterization and are not production tested.
- r_{ON} matching between channels is calculated by subtracting the channel with the highest max r_{ON} value from the channel with lowest max r_{ON} value.
- Crosstalk is inversely proportional to source impedance.

DC ELECTRICAL CHARACTERISTICS – Digital Section (Voltages referenced to GND): $V_{DD} = +3.0\text{ V to }+3.6\text{ V}$, $GND = 0\text{ V}$, $V_S = 2\text{ V}_{RMS}$, $R_{LOAD} = 20\text{ k}\Omega$, $f = 1\text{ kHz}$, $V_{SELH} = V_{ENH} = 1.4\text{ V}$, $V_{SELL} = V_{ENL} = 0.5\text{ V}$, (Note 9), Unless otherwise specified.

Parameter	Test Conditions	Supply (V)	Temp (°C)	Min (Notes 10, 11)	Typ	Max (Notes 10, 11)	Units
DIGITAL INPUT CHARACTERISTICS							
Input Voltage Low, V_{SELL} , V_{ENL}		3.3	Full	–	–	0.5	V
Input Voltage High, V_{SELH} , V_{ENH}		3.3	Full	1.4	–	–	V
Input Current, I_{SELH} , I_{SELL}	$V_{EN} = 0\text{ V}$, $V_{SEL} = 0\text{ V}$ or V_{DD}	3.6	Full	–0.5	0.01	0.5	μA
Input Current, I_{ENL}	$V_{SEL} = V_{DD}$, $V_{EN} = 0\text{ V}$	3.6	Full	–1.3	–0.7	0.3	μA
Input Current, I_{ENH}	$V_{SEL} = 0\text{ V}$, $V_{EN} = V_{DD}$	3.6	Full	–0.5	0.01	0.5	μA

- V_{IN} = input voltage to perform proper function.
- The algebraic convention, whereby the most negative value is a minimum and the most positive a maximum, is used in this data sheet.
- Parameters with MIN and/or MAX limits are 100% tested at +25°C, unless otherwise specified. Temperature limits established by characterization and are not production tested.

TEST CIRCUITS AND WAVEFORMS

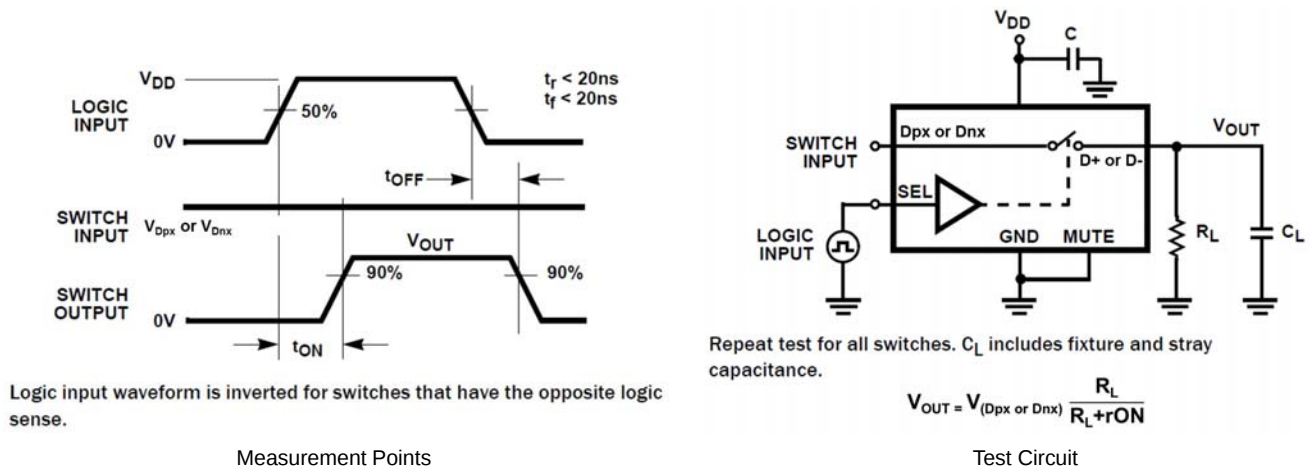


Figure 2. Switching Times

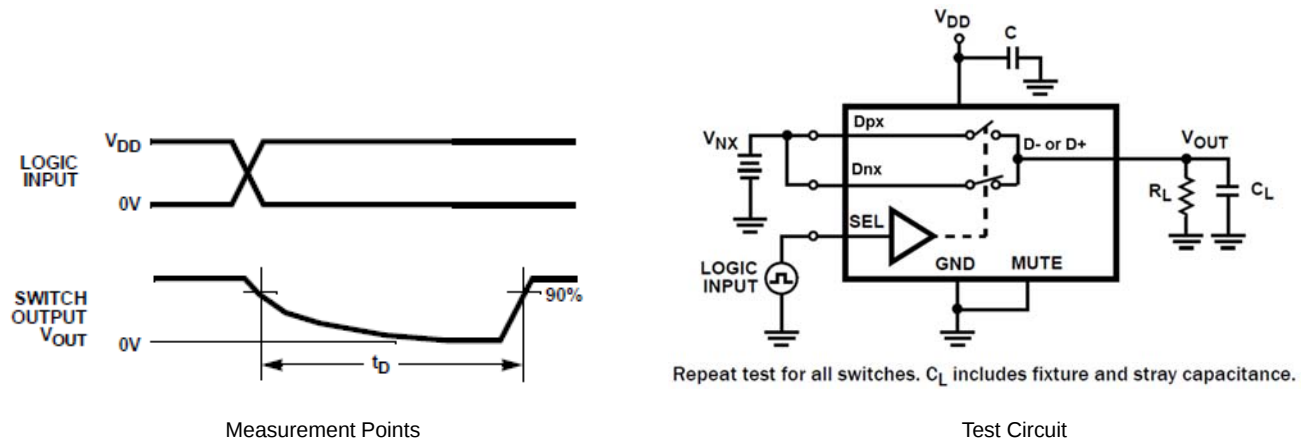


Figure 3. Break-Before-Make Time

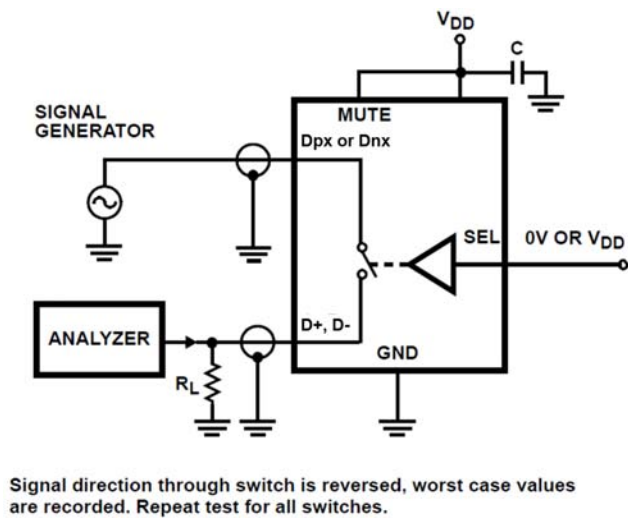


Figure 4. Off-Isolation Test Circuit

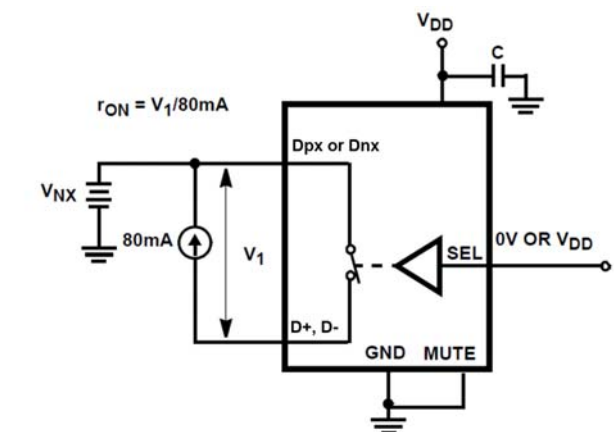
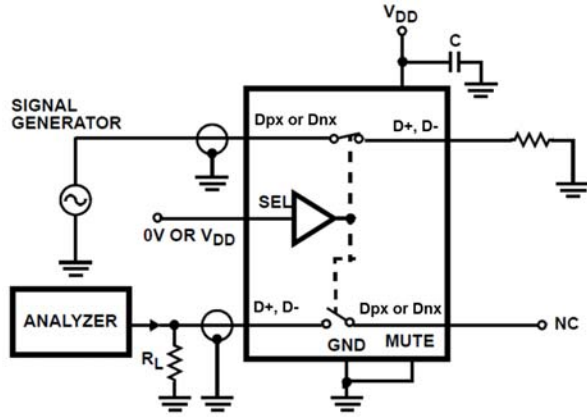


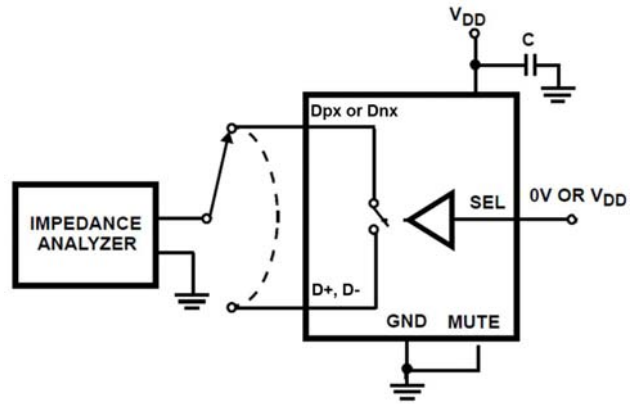
Figure 5. r_{ON} Test Circuit

TEST CIRCUITS AND WAVEFORMS



Signal direction through switch is reversed, worst case values are recorded. Repeat test for all switches.

Figure 6. Crosstalk Test Circuit



Repeat test for all switches.

Figure 7. Capacitance Test Circuit

TYPICAL PERFORMANCE CURVES:

$T_A = +25^\circ\text{C}$, Unless Otherwise Specified

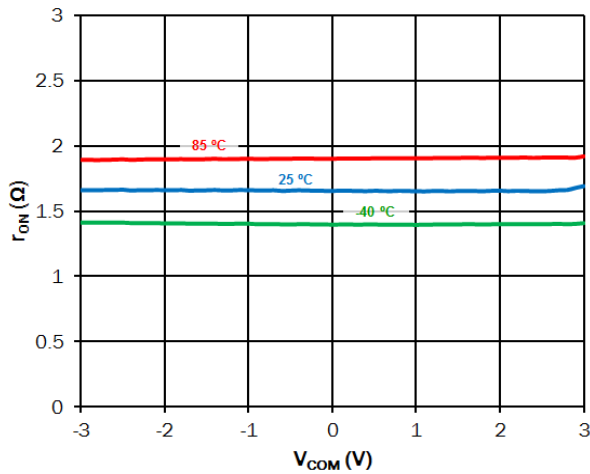


Figure 8. On-Resistance vs. Switch Voltage

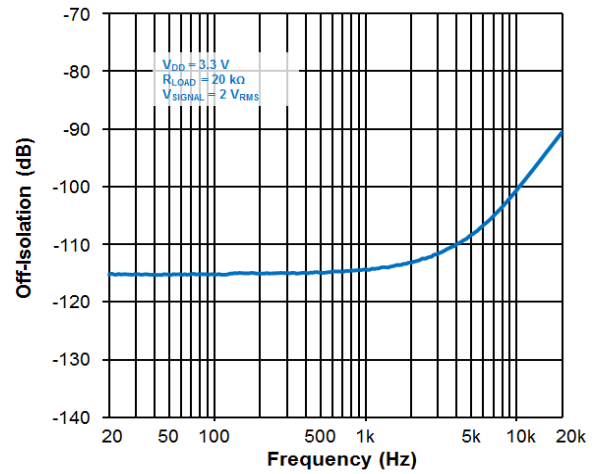


Figure 9. Off-Isolation, 2 V_{RMS} Signal, 20 $k\Omega$ Load

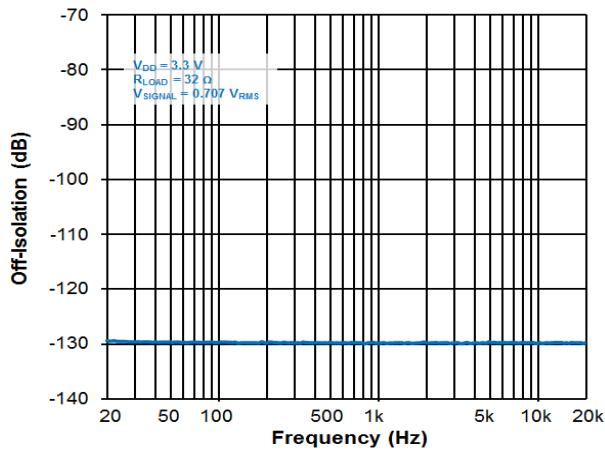


Figure 10. Off-Isolation, 0.707 V_{RMS} Signal, 32 $k\Omega$ Load

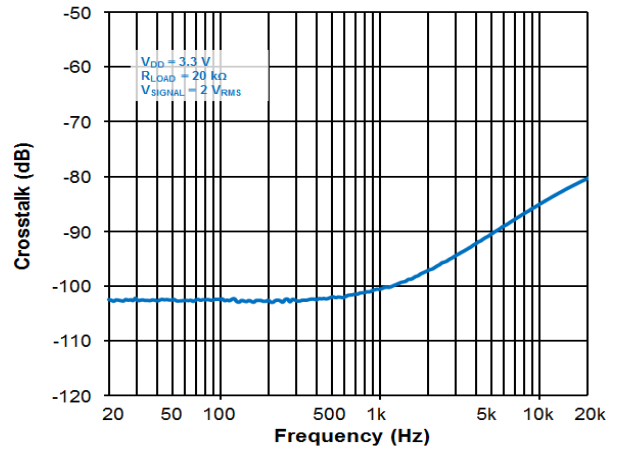


Figure 11. Channel-to-Channel Crosstalk

TYPICAL PERFORMANCE CURVES:

$T_A = +25^\circ\text{C}$, Unless Otherwise Specified

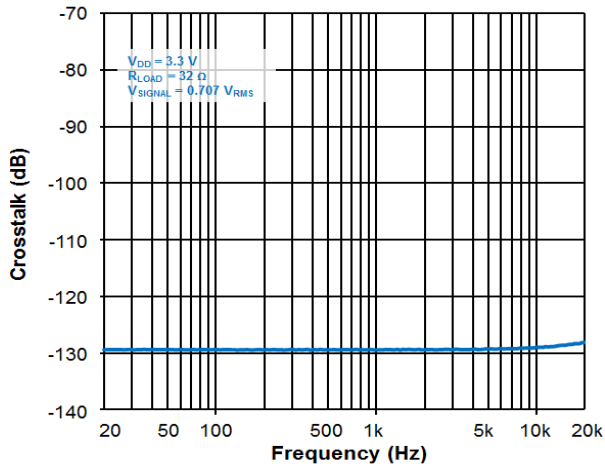


Figure 12. Channel-to-Channel Crosstalk

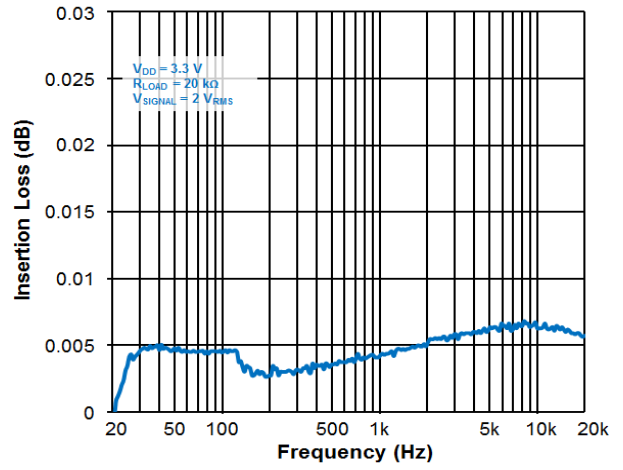


Figure 13. Insertion Loss vs. Frequency

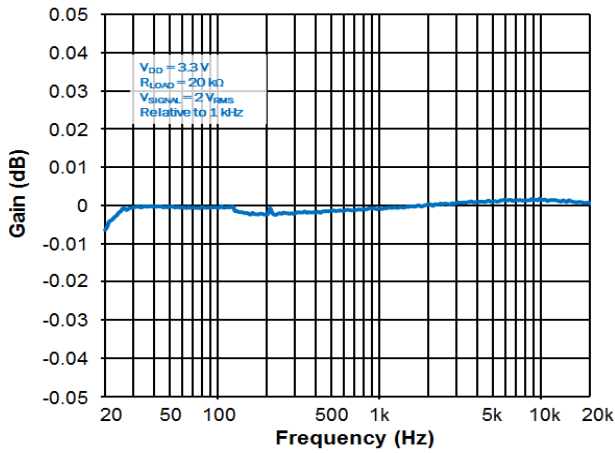


Figure 14. Gain vs. Frequency

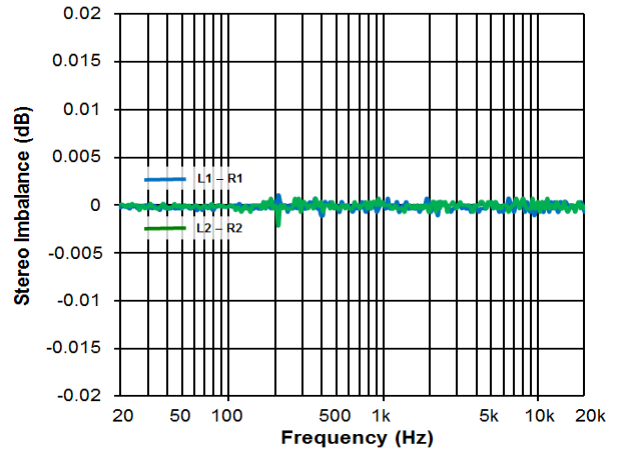


Figure 15. Stereo Imbalance vs. Frequency

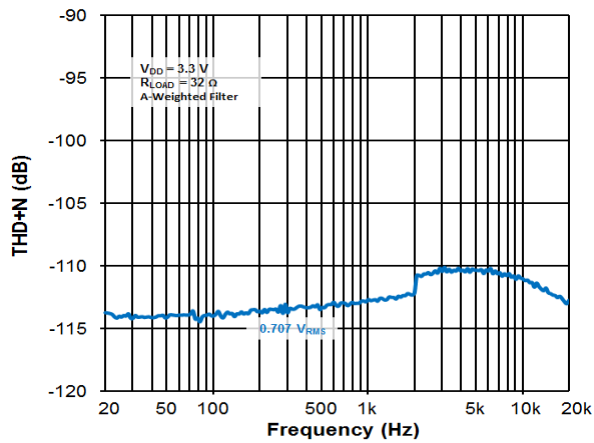


Figure 16. THD+N vs. Signal Levels vs. Frequency

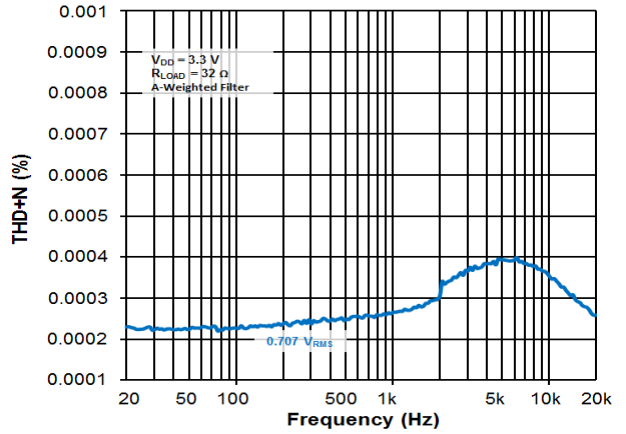


Figure 17. THD+N vs. Signal Levels vs. Frequency

TYPICAL PERFORMANCE CURVES:

$T_A = +25^\circ\text{C}$, Unless Otherwise Specified

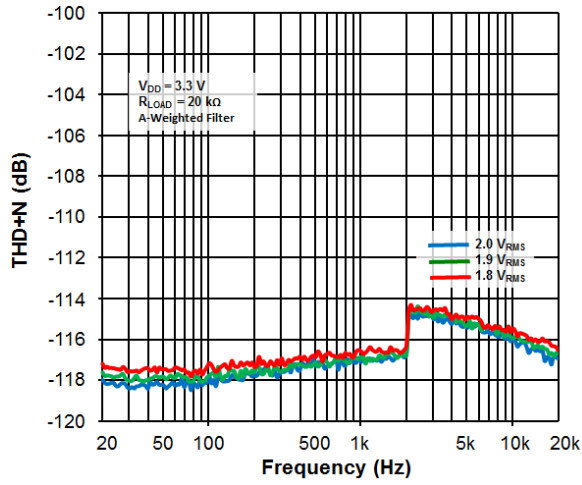


Figure 18. THD+N vs. Signal Levels vs. Frequency

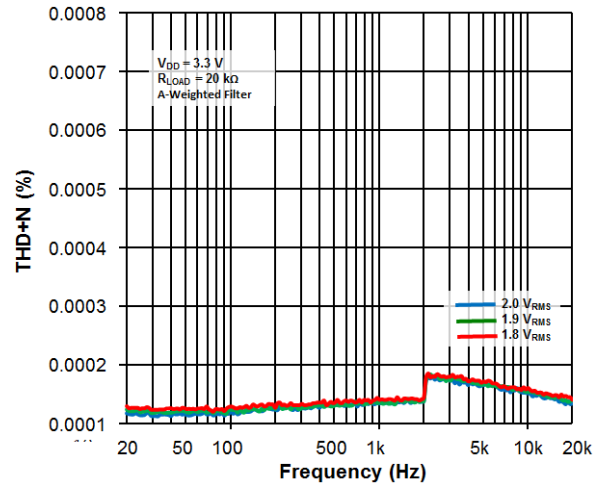


Figure 19. THD+N vs. Signal Levels vs. Frequency

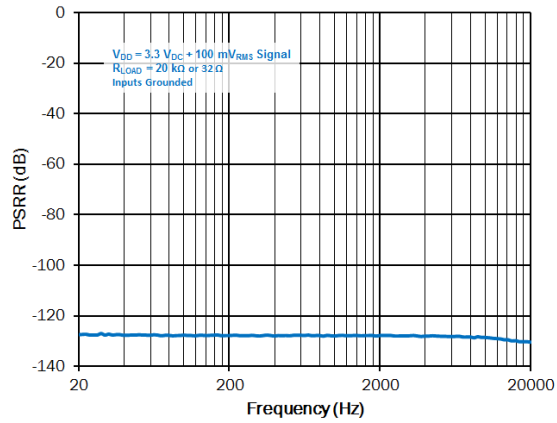


Figure 20. PSRR vs. Frequency

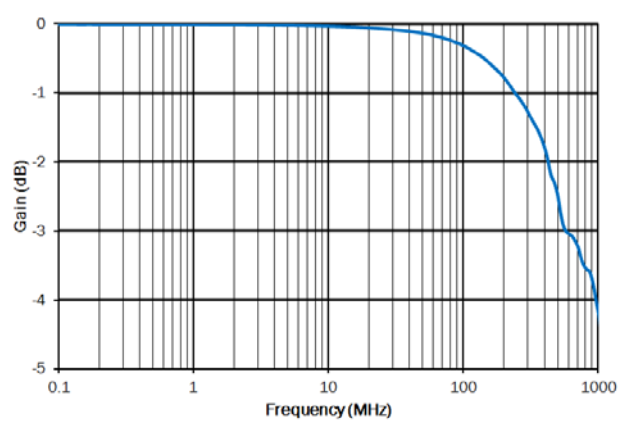


Figure 21. Frequency Response

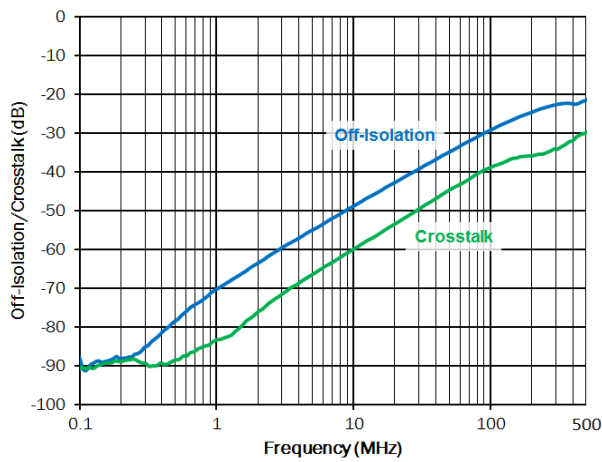


Figure 22. Crosstalk and Off-Isolation

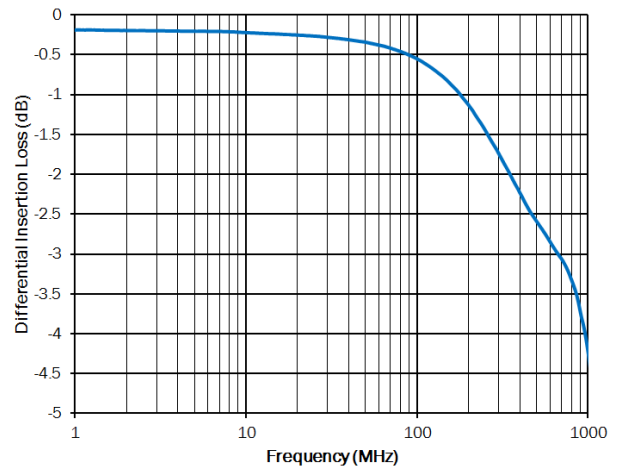


Figure 23. Differential Crosstalk

NL3S588

TYPICAL PERFORMANCE CURVES:

$T_A = +25^{\circ}\text{C}$, Unless Otherwise Specified

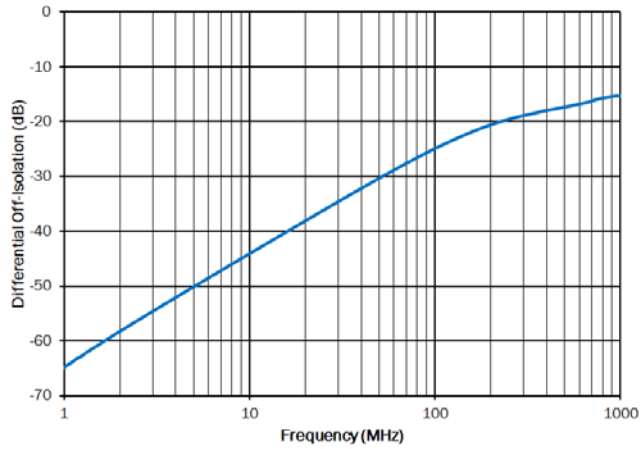


Figure 24. Differential Off-Isolation

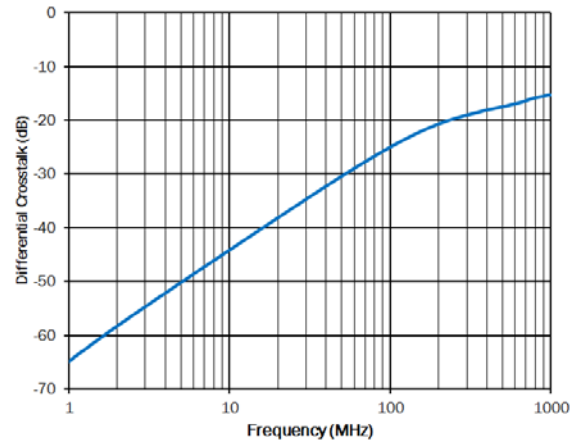


Figure 25. Differential Crosstalk

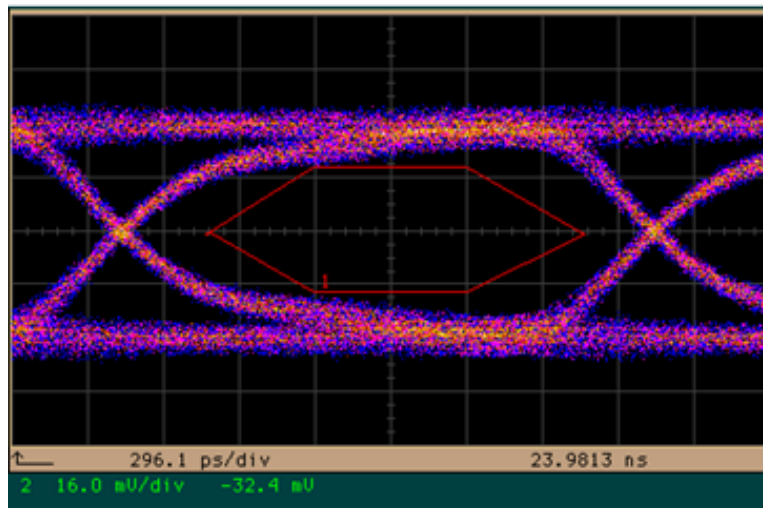
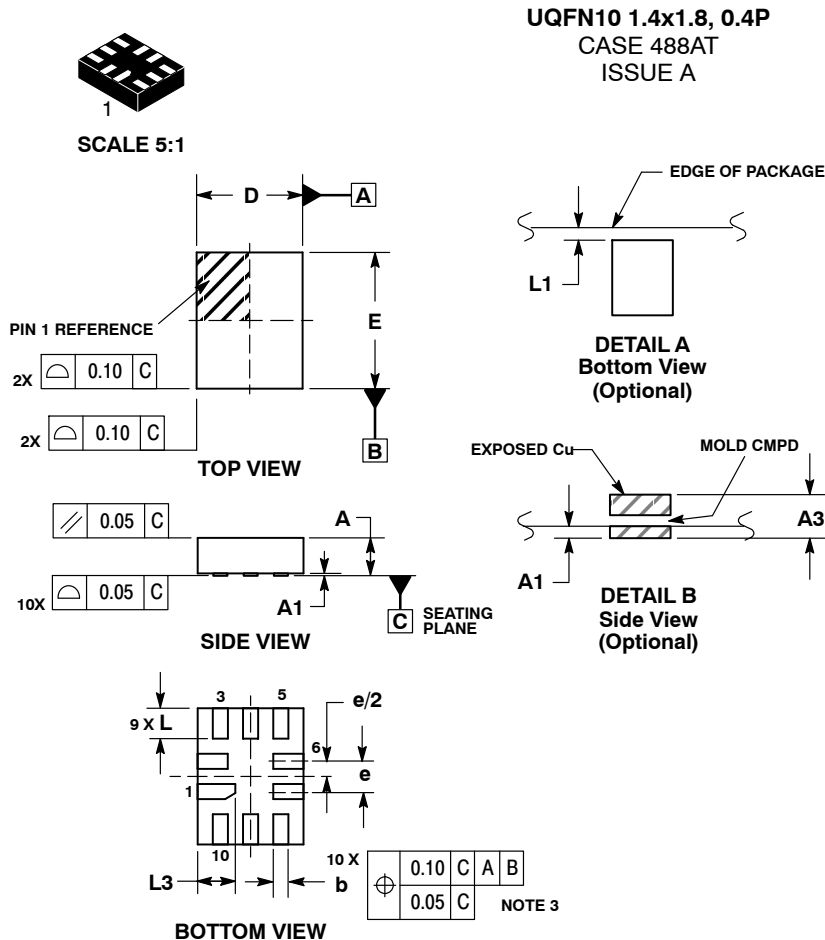


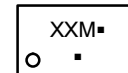
Figure 26. USB 2.0 High-Speed Eye Diagram



- NOTES:
1. DIMENSIONING AND TOLERANCING PER ASME Y14.5M, 1994.
 2. CONTROLLING DIMENSION: MILLIMETERS
 3. DIMENSION b APPLIES TO PLATED TERMINAL AND IS MEASURED BETWEEN 0.25 AND 0.30 MM FROM TERMINAL.
 4. COPLANARITY APPLIES TO THE EXPOSED PAD AS WELL AS THE TERMINALS.

DIM	MILLIMETERS	
	MIN	MAX
A	0.45	0.60
A1	0.00	0.05
A3	0.127 REF	
b	0.15	0.25
D	1.40 BSC	
E	1.80 BSC	
e	0.40 BSC	
L	0.30	0.50
L1	0.00	0.15
L3	0.40	0.60

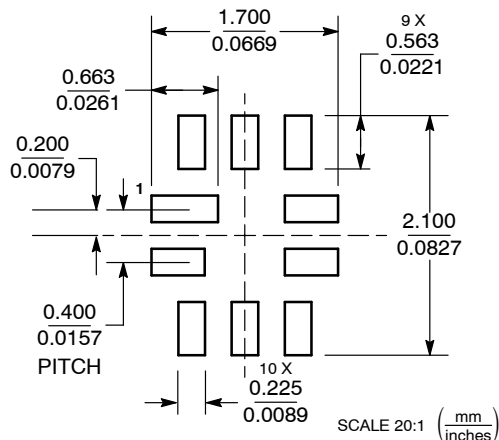
GENERIC MARKING DIAGRAM*



- XX = Specific Device Code
M = Date Code
▪ = Pb-Free Package
(Note: Microdot may be in either location)

*This information is generic. Please refer to device data sheet for actual part marking. Pb-Free indicator, "G" or microdot "▪", may or may not be present.

MOUNTING FOOTPRINT



DOCUMENT NUMBER:	98AON22493D	Electronic versions are uncontrolled except when accessed directly from the Document Repository. Printed versions are uncontrolled except when stamped "CONTROLLED COPY" in red.
DESCRIPTION:	10 PIN UQFN, 1.4 X 1.8, 0.4P	PAGE 1 OF 1

onsemi and Onsemi are trademarks of Semiconductor Components Industries, LLC dba onsemi or its subsidiaries in the United States and/or other countries. onsemi reserves the right to make changes without further notice to any products herein. onsemi makes no warranty, representation or guarantee regarding the suitability of its products for any particular purpose, nor does onsemi assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. onsemi does not convey any license under its patent rights nor the rights of others.

onsemi, **Onsemi**, and other names, marks, and brands are registered and/or common law trademarks of Semiconductor Components Industries, LLC dba "**onsemi**" or its affiliates and/or subsidiaries in the United States and/or other countries. **onsemi** owns the rights to a number of patents, trademarks, copyrights, trade secrets, and other intellectual property. A listing of **onsemi**'s product/patent coverage may be accessed at www.onsemi.com/site/pdf/Patent-Marking.pdf. **onsemi** reserves the right to make changes at any time to any products or information herein, without notice. The information herein is provided "as-is" and **onsemi** makes no warranty, representation or guarantee regarding the accuracy of the information, product features, availability, functionality, or suitability of its products for any particular purpose, nor does **onsemi** assume any liability arising out of the application or use of any product or circuit, and specifically disclaims any and all liability, including without limitation special, consequential or incidental damages. Buyer is responsible for its products and applications using **onsemi** products, including compliance with all laws, regulations and safety requirements or standards, regardless of any support or applications information provided by **onsemi**. "Typical" parameters which may be provided in **onsemi** data sheets and/or specifications can and do vary in different applications and actual performance may vary over time. All operating parameters, including "Typicals" must be validated for each customer application by customer's technical experts. **onsemi** does not convey any license under any of its intellectual property rights nor the rights of others. **onsemi** products are not designed, intended, or authorized for use as a critical component in life support systems or any FDA Class 3 medical devices or medical devices with a same or similar classification in a foreign jurisdiction or any devices intended for implantation in the human body. Should Buyer purchase or use **onsemi** products for any such unintended or unauthorized application, Buyer shall indemnify and hold **onsemi** and its officers, employees, subsidiaries, affiliates, and distributors harmless against all claims, costs, damages, and expenses, and reasonable attorney fees arising out of, directly or indirectly, any claim of personal injury or death associated with such unintended or unauthorized use, even if such claim alleges that **onsemi** was negligent regarding the design or manufacture of the part. **onsemi** is an Equal Opportunity/Affirmative Action Employer. This literature is subject to all applicable copyright laws and is not for resale in any manner.

ADDITIONAL INFORMATION

TECHNICAL PUBLICATIONS:

Technical Library: www.onsemi.com/design/resources/technical-documentation
onsemi Website: www.onsemi.com

ONLINE SUPPORT: www.onsemi.com/support

For additional information, please contact your local Sales Representative at
www.onsemi.com/support/sales