



NJM7800

3-TERMINAL POSITIVE VOLTAGE REGULATOR

FEATURES

- Internal Short Circuit Current Limit
- Excellent Ripple Rejection
- Guarantee'd 1.5A Output Current
- Package Outline TO-220F, TO-252
- Bipolar Technology

GENERAL DESCRIPTION

The NJM7800 series of monolithic 3-Terminal Positive Voltage Regulators is constructed using the Nisshinbo Micro Devices planar epitaxial process. These regulators employ internal current-limiting, thermal-shutdown and safe-area compensation making them essentially indestructible. If adequate heat sinking is provided, they can deliver over 1A output current. They are intended as fixed voltage regulators in a wide range of applications including local (on card) regulation for elimination of distribution problems associated with single point regulation. In addition to use as fixed voltage regulators, these devices can be used with external components to obtain adjustable output voltages and currents.

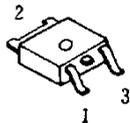
(TO-220F)



NJM7800FA

- 1. IN
- 2. GND
- 3. OUT

(TO-252)

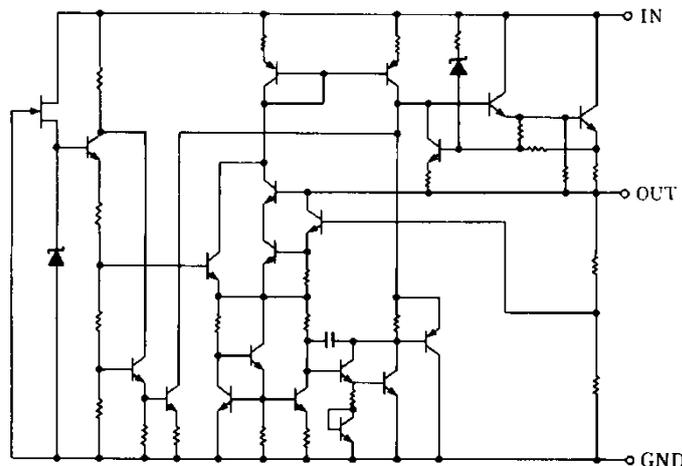


NJM7800DL1A

- 1. IN
- 2. GND
- 3. OUT

(note) The radiation fin is connected pin2.

EQUIVALENT CIRCUIT



■ ABSOLUTE MAXIMUM RATINGS

(Ta=25°C)

PARAMETER	SYMBOL	MAXIMUM RATINGS		UNIT
Input Voltage	V _{IN}	7805 to 7810 7812 to 7815 7818 to 7824	35 35 40	V
Storage Temperature Range	T _{stg}	-40 to +150		°C
Operating Temperature Range	Operating Junction Temperature Operating Junction Temperature	T _j T _{opr}	-40 to +150 -40 to +85	°C
Power Dissipation	P _D	TO-220F TO-252	16(T _C ≤70°C) *1 10(T _C =25°C) *1	W

*1) Infinite Heat Sink

■ THERMAL CHARACTERISTICS

Package	Measurement Result		Unit
	Thermal Resistance (θ _{ja})	Thermal Characterization Parameter (ψ _{jt})	
TO-252	141 °C/W	32 °C/W	°C/W

θ_{ja}: Junction-to-Ambient Thermal Resistance, ψ_{jt}: Junction-to-Top Thermal Characterization Parameter

*2) 2-Layer: Mounted on glass epoxy board (76.2 mm × 114.3 mm × 1.6 mm: based on EIA/JEDEC standard, 2-layer FR-4), Cu area: 100 mm²

*3) 4-Layer: Mounted on glass epoxy board (76.2 mm × 114.3 mm × 1.6 mm: based on EIA/JEDEC standard, 4-layer FR-4).

(For 4-layer: Applying 74.2 mm × 74.2 mm inner Cu area and a thermal via hole to a board based on JEDEC standard JESD51-5.)

■ ELECTRICAL CHARACTERISTICS (C₁=0.33μF, C_O=0.1μF, T_F=25°C)

Measurement is to be conducted in pulse testing.

PARAMETER	SYMBOL	TEST CONDITIONS	TO-220F			TO-252			UNIT
			MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	
NJM7805FA/DL1A									
Output Voltage	V _O	V _{IN} =10V, I _O =0.5A	4.8	5.0	5.2	4.8	5.0	5.2	V
Line Regulation	ΔV _O - V _{IN}	V _{IN} =7 to 25V, I _O =0.5A	-	3	50	-	3	100	mV
Load Regulation	ΔV _O - I _O	V _{IN} =10V, I _O =0.005 to 1.5A	-	15	50	-	15	100	mV
Quiescent Current	I _Q	V _{IN} =10V, I _O =0mA	-	4.2	6.0	-	4.2	6.0	mA
Average Temperature Coefficient of Output Voltage	ΔV _O /ΔT	V _{IN} =10V, I _O =5mA	-	-0.5	-	-	-0.5	-	mV/°C
Ripple Rejection	RR	V _{IN} =10V, I _O =0.5A, e _{in} =2V _{P-P} , f=120Hz	68	78	-	68	78	-	dB
Output Noise Voltage	V _{NO}	V _{IN} =10V, BW=10Hz to 100kHz, I _O =0.5A	-	45	-	-	45	-	μV
NJM7806FA/DL1A									
Output Voltage	V _O	V _{IN} =11V, I _O =0.5A	5.75	6.0	6.25	5.75	6.0	6.25	V
Line Regulation	ΔV _O - V _{IN}	V _{IN} =8 to 25V, I _O =0.5A	-	5	60	-	5	120	mV
Load Regulation	ΔV _O - I _O	V _{IN} =11V, I _O =0.005 to 1.5A	-	15	60	-	15	120	mV
Quiescent Current	I _Q	V _{IN} =11V, I _O =0mA	-	4.3	6.0	-	4.3	6.0	mA
Average Temperature Coefficient of Output Voltage	ΔV _O /ΔT	V _{IN} =11V, I _O =5mA	-	-0.6	-	-	-0.6	-	mV/°C
Ripple Rejection	RR	V _{IN} =11V, I _O =0.5A, e _{in} =2V _{P-P} , f=120Hz	65	75	-	65	75	-	dB
Output Noise Voltage	V _{NO}	V _{IN} =11V, BW=10Hz to 100kHz, I _O =0.5A	-	45	-	-	45	-	μV

■ ELECTRICAL CHARACTERISTICS (C₁=0.33μF, C_O=0.1μF, T_F=25°C)

Measurement is to be conducted in pulse testing.

PARAMETER	SYMBOL	TEST CONDITIONS	TO-220F			TO-252			UNIT
			MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	
NJM7808FA/DL1A									
Output Voltage	V _O	V _{IN} =14V, I _O =0.5A	7.7	8.0	8.3	7.7	8.0	8.3	V
Line Regulation	ΔV _O - V _{IN}	V _{IN} =10.5 to 25V, I _O =0.5A	-	6	80	-	6	160	mV
Load Regulation	ΔV _O - I _O	V _{IN} =14V, I _O =0.005 to 1.5A	-	15	80	-	15	160	mV
Quiescent Current	I _Q	V _{IN} =14V, I _O =0mA	-	4.3	6.0	-	4.3	6.0	mA
Average Temperature Coefficient of Output Voltage	ΔV _O /ΔT	V _{IN} =14V, I _O =5mA	-	-0.8	-	-	-0.8	-	mV/°C
Ripple Rejection	RR	V _{IN} =14V, I _O =0.5A, e _{in} =2V _{P-P} , f=120Hz	62	72	-	62	72	-	dB
Output Noise Voltage	V _{NO}	V _{IN} =14V, BW=10Hz to 100kHz, I _O =0.5A	-	55	-	-	55	-	μV
NJM7809FA/DL1A									
Output Voltage	V _O	V _{IN} =15V, I _O =0.5A	8.65	9.0	9.35	8.65	9.0	9.35	V
Line Regulation	ΔV _O - V _{IN}	V _{IN} =11.5 to 25V, I _O =0.5A	-	7	90	-	7	180	mV
Load Regulation	ΔV _O - I _O	V _{IN} =15V, I _O =0.005 to 1.5A	-	15	90	-	15	180	mV
Quiescent Current	I _Q	V _{IN} =15V, I _O =0mA	-	4.3	6.0	-	4.3	6.0	mA
Average Temperature Coefficient of Output Voltage	ΔV _O /ΔT	V _{IN} =15V, I _O =5mA	-	-0.9	-	-	-0.9	-	mV/°C
Ripple Rejection	RR	V _{IN} =15V, I _O =0.5A, e _{in} =2V _{P-P} , f=120Hz	62	72	-	62	72	-	dB
Output Noise Voltage	V _{NO}	V _{IN} =15V, BW=10Hz to 100kHz, I _O =0.5A	-	60	-	-	60	-	μV
NJM7810FA/DL1A									
Output Voltage	V _O	V _{IN} =17V, I _O =0.5A	9.60	10.0	10.4	9.6	10.0	10.4	V
Line Regulation	ΔV _O - V _{IN}	V _{IN} =12.5 to 25V, I _O =0.5A	-	7	100	-	7	200	mV
Load Regulation	ΔV _O - I _O	V _{IN} =17V, I _O =0.005 to 1.5A	-	15	130	-	15	200	mV
Quiescent Current	I _Q	V _{IN} =17V, I _O =0mA	-	4.3	6.0	-	4.3	6.0	mA
Average Temperature Coefficient of Output Voltage	ΔV _O /ΔT	V _{IN} =17V, I _O =5mA	-	-0.9	-	-	-1.0	-	mV/°C
Ripple Rejection	RR	V _{IN} =17V, I _O =0.5A, e _{in} =2V _{P-P} , f=120Hz	62	72	-	62	72	-	dB
Output Noise Voltage	V _{NO}	V _{IN} =17V, BW=10Hz to 100kHz, I _O =0.5A	-	60	-	-	65	-	μV

■ ELECTRICAL CHARACTERISTICS (C₁=0.33μF, C_O=0.1μF, T_J=25°C)

Measurement is to be conducted in pulse testing.

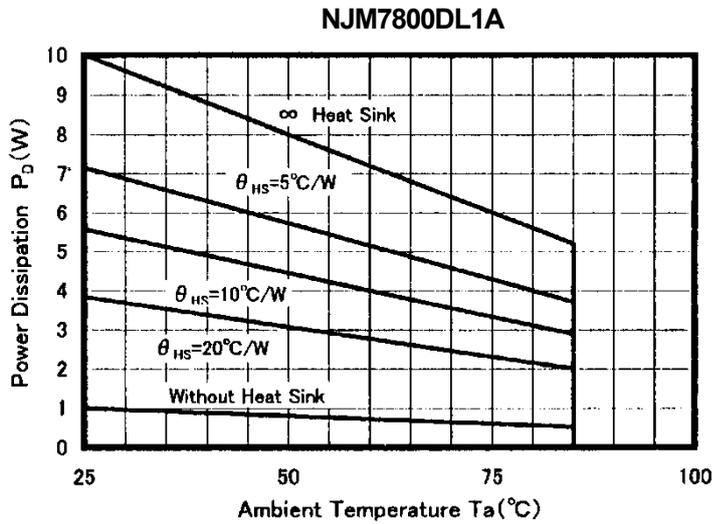
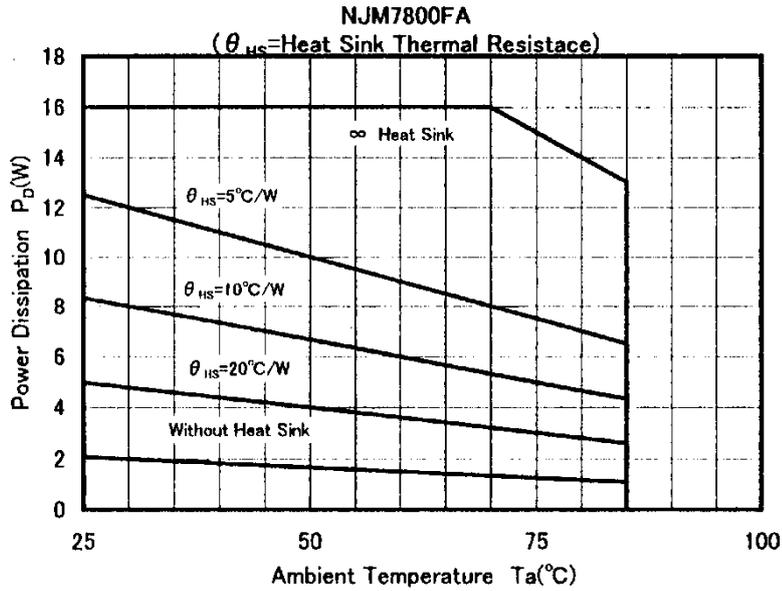
PARAMETER	SYMBOL	TEST CONDITIONS	TO-220F			TO-252			UNIT
			MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	
NJM7812FA/DL1A									
Output Voltage	V _O	V _{IN} =19V, I _O =0.5A	11.5	12.0	12.5	11.5	12.0	12.5	V
Line Regulation	ΔV _O - V _{IN}	V _{IN} =14.5 to 30V, I _O =0.5A	-	10	120	-	10	240	mV
Load Regulation	ΔV _O - I _O	V _{IN} =19V, I _O =0.005 to 1.5A	-	25	120	-	25	240	mV
Quiescent Current	I _Q	V _{IN} =19V, I _O =0mA	-	4.3	6.0	-	4.3	6.0	mA
Average Temperature Coefficient of Output Voltage	ΔV _O /ΔT	V _{IN} =19V, I _O =5mA	-	-1.2	-	-	-1.2	-	mV/°C
Ripple Rejection	RR	V _{IN} =19V, I _O =0.5A, e _{in} =2V _{P-P} , f=120Hz	61	71	-	61	71	-	dB
Output Noise Voltage	V _{NO}	V _{IN} =19V, BW=10Hz to 100kHz, I _O =0.5A	-	75	-	-	75	-	μV
NJM7815FA/DL1A									
Output Voltage	V _O	V _{IN} =23V, I _O =0.5A	14.4	15.0	15.6	14.4	15.0	15.6	V
Line Regulation	ΔV _O - V _{IN}	V _{IN} =17.5 to 30V, I _O =0.5A	-	11	150	-	11	300	mV
Load Regulation	ΔV _O - I _O	V _{IN} =23V, I _O =0.005 to 1.5A	-	35	150	-	35	300	mV
Quiescent Current	I _Q	V _{IN} =23V, I _O =0mA	-	4.4	6.0	-	4.4	6.0	mA
Average Temperature Coefficient of Output Voltage	ΔV _O /ΔT	V _{IN} =23V, I _O =5mA	-	-1.5	-	-	-1.5	-	mV/°C
Ripple Rejection	RR	V _{IN} =23V, I _O =0.5A, e _{in} =2V _{P-P} , f=120Hz	60	70	-	60	70	-	dB
Output Noise Voltage	V _{NO}	V _{IN} =23V, BW=10Hz to 100kHz, I _O =0.5A	-	90	-	-	90	-	μV
NJM7818FA/DL1A									
Output Voltage	V _O	V _{IN} =27V, I _O =0.5A	17.3	18.0	18.7	17.3	18.0	18.7	V
Line Regulation	ΔV _O - V _{IN}	V _{IN} =21 to 33V, I _O =0.5A	-	15	180	-	15	360	mV
Load Regulation	ΔV _O - I _O	V _{IN} =27V, I _O =0.005 to 1.5A	-	55	180	-	55	360	mV
Quiescent Current	I _Q	V _{IN} =27V, I _O =0mA	-	4.5	6.0	-	4.5	6.0	mA
Average Temperature Coefficient of Output Voltage	ΔV _O /ΔT	V _{IN} =27V, I _O =5mA	-	-1.8	-	-	-1.8	-	mV/°C
Ripple Rejection	RR	V _{IN} =27V, I _O =0.5A, e _{in} =2V _{P-P} , f=120Hz	59	69	-	59	69	-	dB
Output Noise Voltage	V _{NO}	V _{IN} =27V, BW=10Hz to 100kHz, I _O =0.5A	-	100	-	-	100	-	μV
NJM7820FA/DL1A									
Output Voltage	V _O	V _{IN} =29V, I _O =0.5A	19.2	20.0	20.8	19.2	20.0	20.8	V
Line Regulation	ΔV _O - V _{IN}	V _{IN} =23 to 35V, I _O =0.5A	-	16	200	-	16	400	mV
Load Regulation	ΔV _O - I _O	V _{IN} =29V, I _O =0.005 to 1.5A	-	61	200	-	61	400	mV
Quiescent Current	I _Q	V _{IN} =29V, I _O =0mA	-	4.5	6.0	-	4.5	6.0	mA
Average Temperature Coefficient of Output Voltage	ΔV _O /ΔT	V _{IN} =29V, I _O =5mA	-	-2.0	-	-	-2.0	-	mV/°C
Ripple Rejection	RR	V _{IN} =29V, I _O =0.5A, e _{in} =2V _{P-P} , f=120Hz	58	68	-	58	68	-	dB
Output Noise Voltage	V _{NO}	V _{IN} =29V, BW=10Hz to 100kHz, I _O =0.5A	-	120	-	-	120	-	μV

■ ELECTRICAL CHARACTERISTICS (C₁=0.33μF, C_O=0.1μF, T_F=25°C)

Measurement is to be conducted in pulse testing.

PARAMETER	SYMBOL	TEST CONDITIONS	TO-220F			TO-252			UNIT
			MIN.	TYP.	MAX.	MIN.	TYP.	MAX.	
NJM7824FA/DL1A									
Output Voltage	V _O	V _{IN} =33V, I _O =0.5A	23.0	24.0	25.0	23.0	24.0	25.0	V
Line Regulation	ΔV _O - V _{IN}	V _{IN} =27 to 38V, I _O =0.5A	-	18	240	-	18	480	mV
Load Regulation	ΔV _O - I _O	V _{IN} =33V, I _O =0.005 to 1.5A	-	65	240	-	65	480	mV
Quiescent Current	I _Q	V _{IN} =33V, I _O =0mA	-	4.6	6.0	-	4.6	6.0	mA
Average Temperature Coefficient of Output Voltage	ΔV _O /ΔT	V _{IN} =33V, I _O =5mA	-	-2.4	-	-	-2.4	-	mV/°C
Ripple Rejection	RR	V _{IN} =33V, I _O =0.5A, e _{in} =2V _{P-P} , f=120Hz	56	66	-	56	66	-	dB
Output Noise Voltage	V _{No}	V _{IN} =33V, BW=10Hz to 100kHz, I _O =0.5A	-	120	-	-	120	-	μV

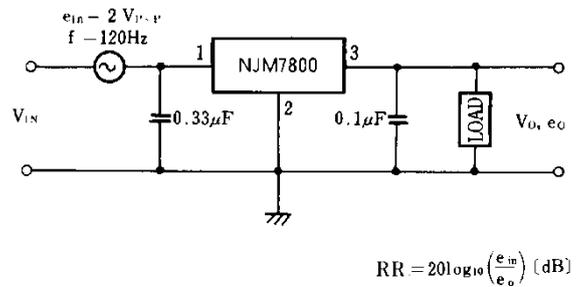
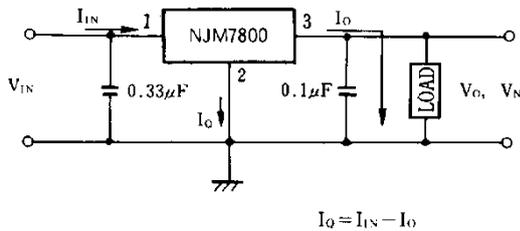
■ POWER DISSIPATION VS. AMBIENT TEMPERATURE



■ TEST CIRCUIT

1. Output Voltage, Line Regulation, Load Regulation, Quiescent Current, Average Temperature Coefficient of Output Voltage, Output Noise Voltage

2. Ripple Rejection



■ Input Capacitor C_{IN}

Input Capacitor C_{IN} is required to prevent oscillation and reduce power supply ripple for applications when high power supply impedance or a long power supply line.

Therefore, use the recommended C_{IN} value (refer to conditions of ELECTRIC CHARACTERISTIC) or larger and should connect between GND and V_{IN} as shortest path as possible to avoid the problem.

■ Output Capacitor C_O

Output capacitor (C_O) will be required for a phase compensation of the internal error amplifier.

The capacitance and the equivalent series resistance (ESR) influence to stable operation of the regulator.

Use of a smaller C_O may cause excess output noise or oscillation of the regulator due to lack of the phase compensation.

On the other hand, Use of a larger C_O reduces output noise and ripple output, and also improves output transient response when rapid load change.

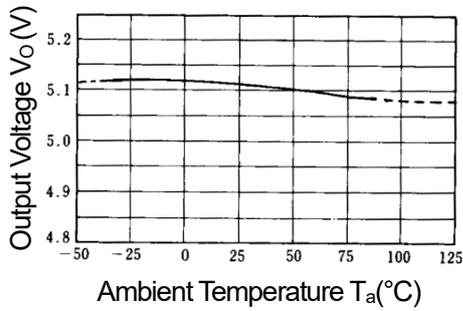
Therefore, use the recommended C_O value (refer to conditions of ELECTRIC CHARACTERISTIC) or larger and should connect between GND and V_{OUT} as shortest path as possible for stable operation

In addition, you should consider varied characteristics of capacitor (a frequency characteristic, a temperature characteristic, a DC bias characteristic and so on) and unevenness peculiar to a capacitor supplier enough.

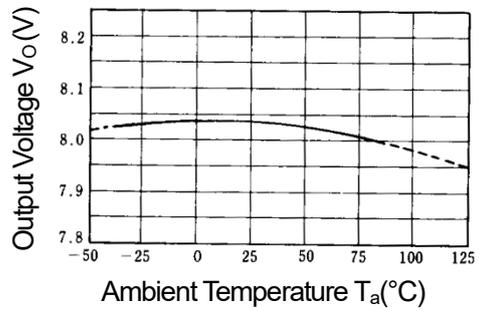
When selecting C_O , recommend that have withstand voltage margin against output voltage and superior temperature characteristic though

■ TYPICAL CHARACTERISTICS

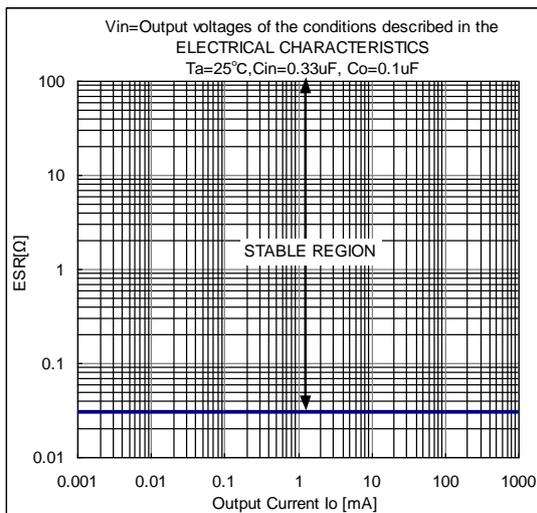
NJM7805 Output Voltage vs. Temperature



NJM7808 Output Voltage vs. Temperature

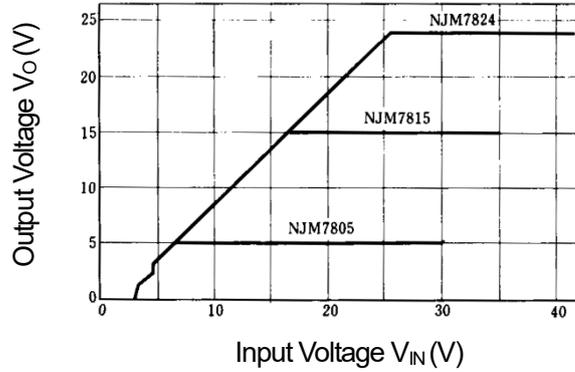


NJM78M00 Series Equivalent Series Resistance vs. Output Current

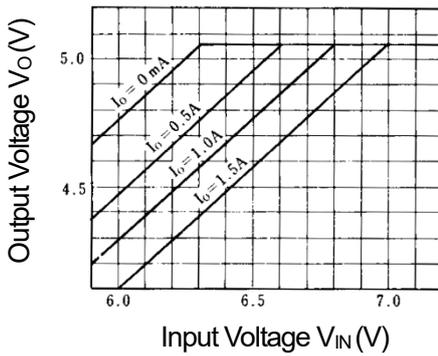


■ TYPICAL CHARACTERISTICS

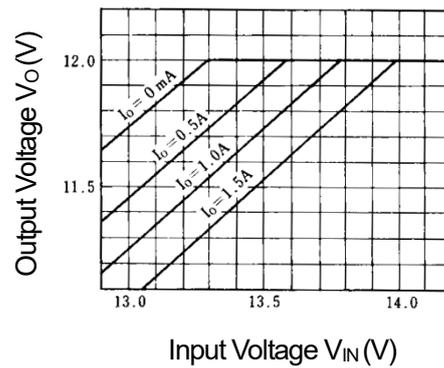
NJM7805/15/24 Output Characteristics
($I_o=0.5A, T_j=25^\circ C$)



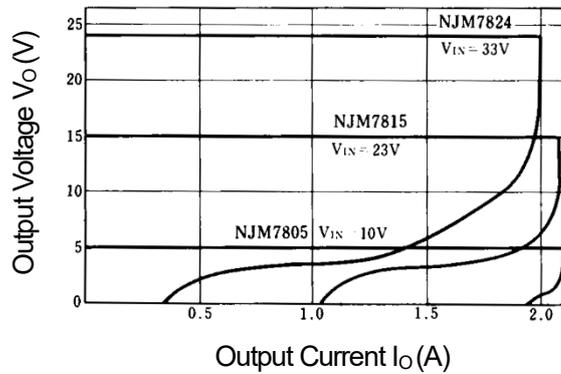
NJM7805 Dropout Characteristics
($T_j=25^\circ C$)



NJM7812 Dropout Characteristics
($T_j=25^\circ C$)

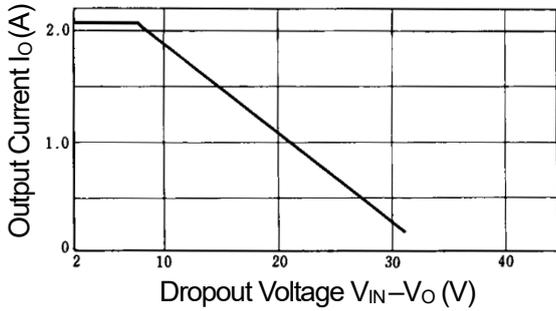


NJM7805/15/24 Load Characteristics
($T_j=25^\circ C$)

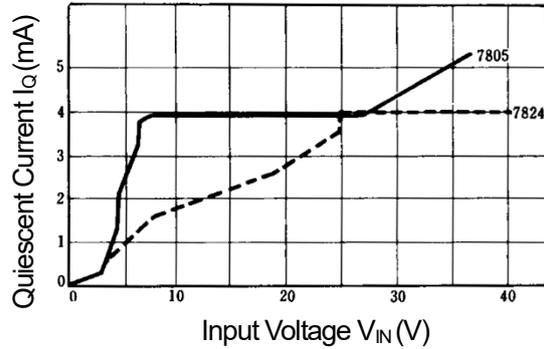


■ TYPICAL CHARACTERISTICS

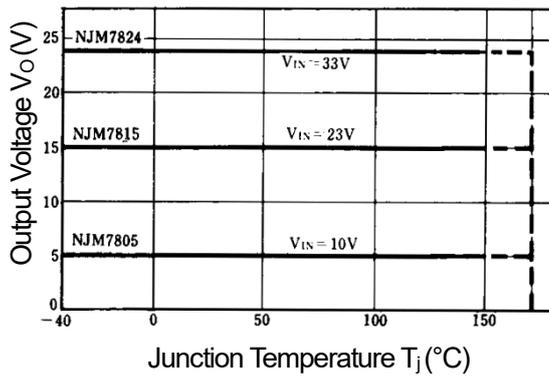
NJM7800 Series Short Circuit Output Current
($T_j=25^\circ\text{C}$, ∞ Heat Sink)



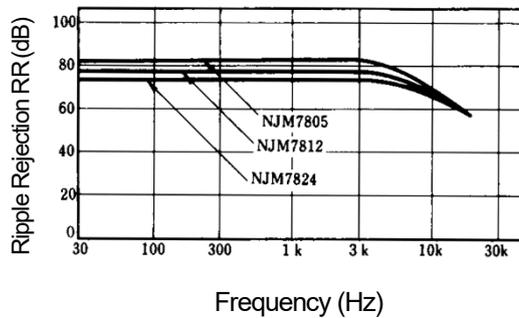
NJM7805/24 Quiescent Current vs. Input Voltage
($T_j=25^\circ\text{C}$,)



NJM7805/15/24 Output Voltage vs. Junction Temperature



NJM7805/15/24 Ripple Rejection vs. Frequency



$V_{IN} = 10\text{V (05)}$ $e_{in} = 2V_{P-P}$
 19V (12)
 33V (24)
 $T_j = 25^\circ\text{C}$

■ REVISION HISTORY

Date	Revision	Changes
January 08,2023	Ver. 1.0	<ul style="list-style-type: none"> •Change of company name and design form. •Change of revision number (Ver.2019-11-14 → Ver.1.0) •Added revision history
Feb.17.2025	Ver. 1.1	<ul style="list-style-type: none"> •NJM7800FA is NRND •Thermal characteristics added